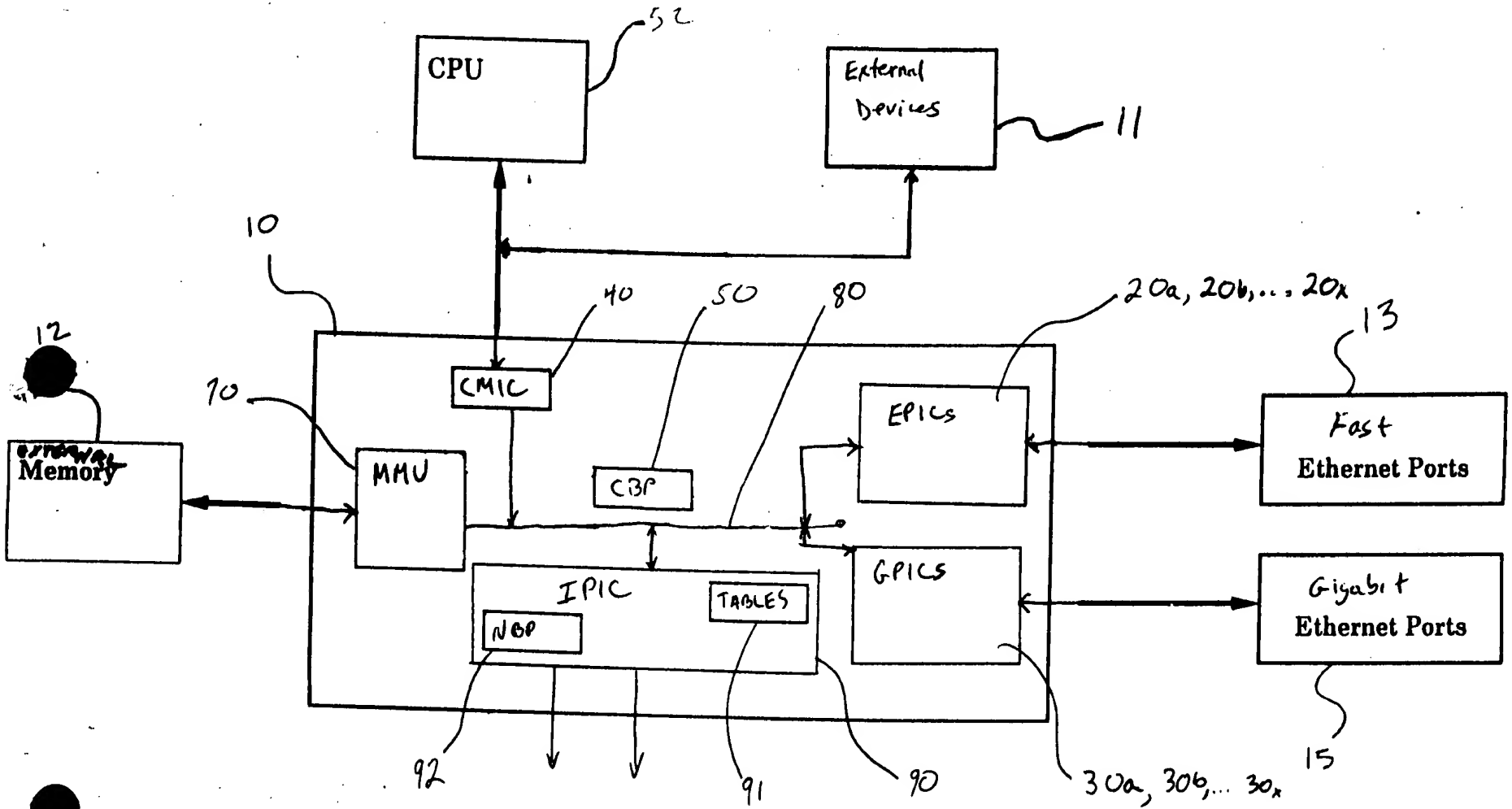

CPU
52
External Devices
11
10
12
Memory
70
MMU
CMIC
40
50
CBP
80
EPICs
20a, 20b, ... 20x
13
Fast Ethernet Ports
GPICs
Gigabit Ethernet Ports
15
IPIC
TABLES
NBP
92
91
90
30a, 30b, ... 30x

Fig. 1

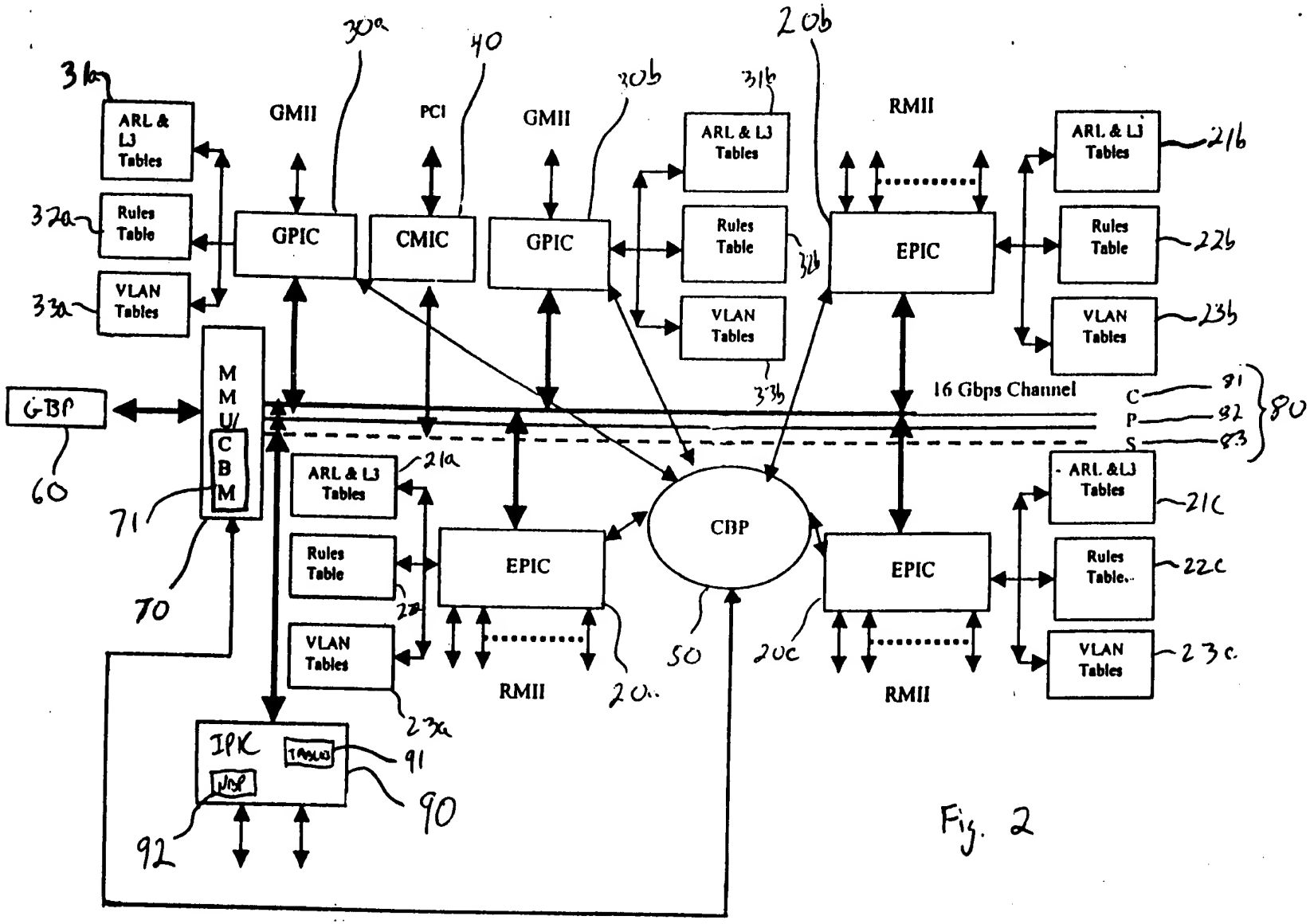

ARL & L3 Tables
Rules Table
VLAN Tables
GMII
PCI
GMII
RMII
GPIC
CMIC
GPIC
EPIC
16 Gbps Channel
C
P
S
GBP
MMU/CBM
CBP
EPIC
EPIC
IPK
RMII
RMII

Fig. 2

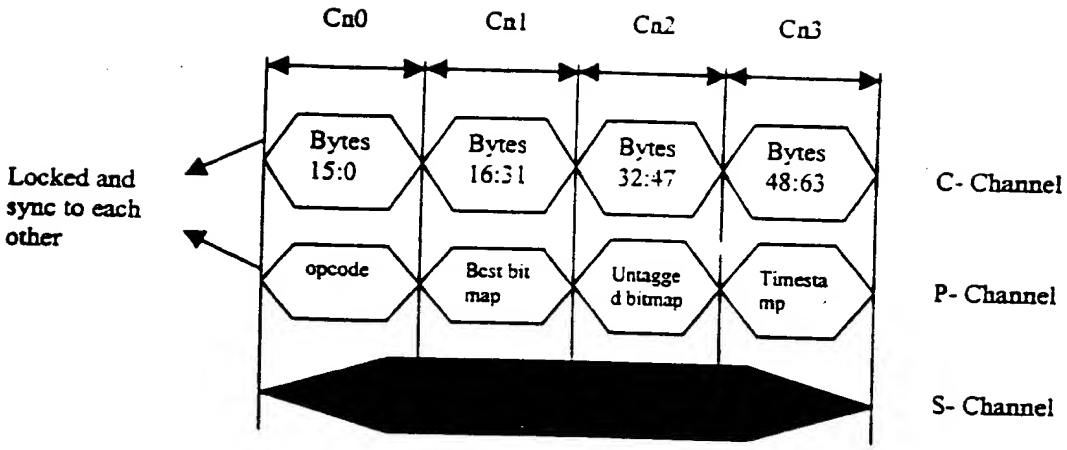

Cn0
Cn1
Cn2
Cn3
Bytes 15:0
Bytes 16:31
Bytes 32:47
Bytes 48:63
Locked and sync to each other
opcode
Best bit map
Untagged bitmap
Timestamp
C- Channel
P- Channel
S- Channel

Fig. 3

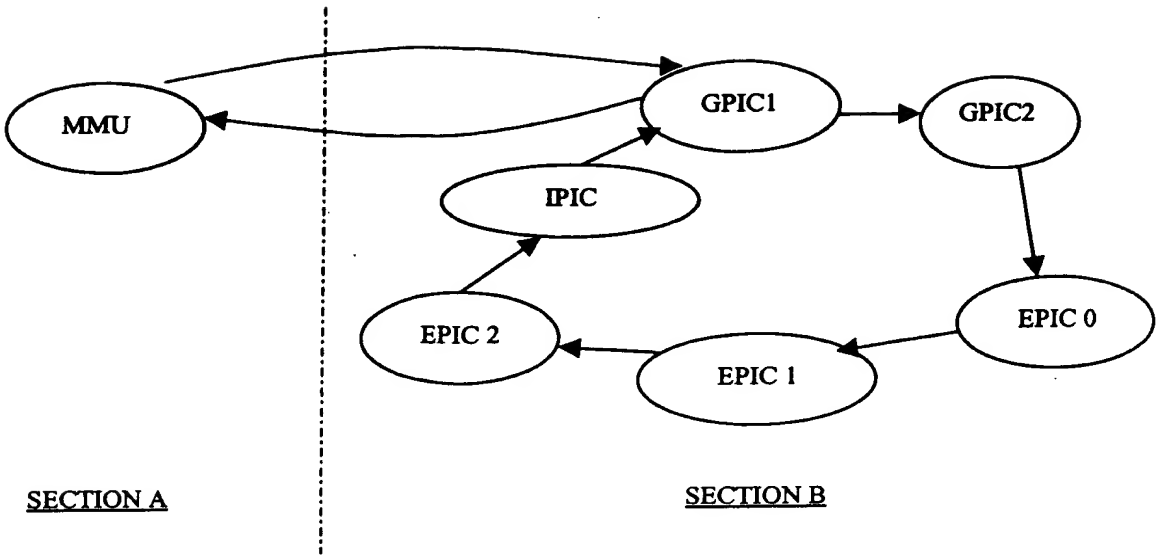

MMU
GPIC1
GPIC2
IPIC
EPIC 0
EPIC 2
EPIC 1
SECTION A
SECTION B

Fig. 4a

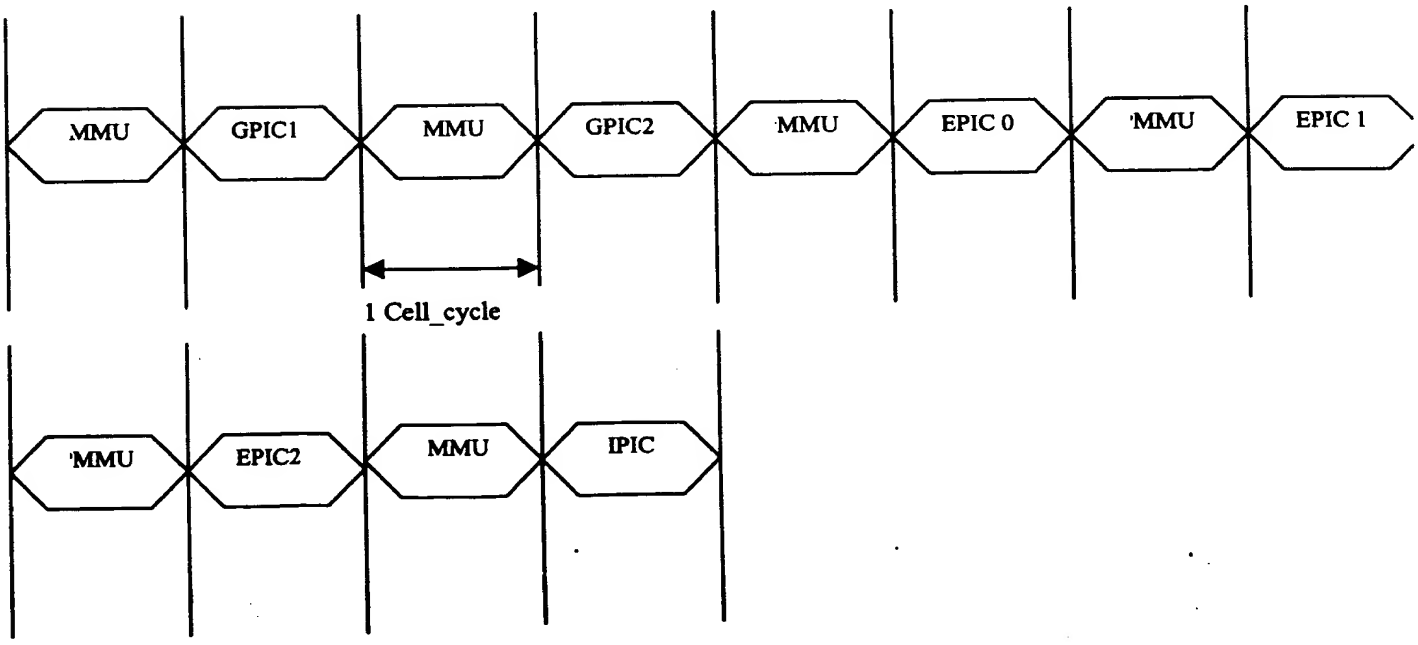

MMU
GPIC1
MMU
GPIC2
MMU
EPIC 0
MMU
EPIC 1
1 Cell_cycle
MMU
EPIC2
MMU
IPIC

Fig. 4b

Protocol Channel Messages

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
Opcode	Ip IPX	Reserved	Nxt cell	Src Dest Port			Cos	J	S	E	Crc	P	O	Len	

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
Module Id Bitmap															

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
R	Bc/Mc Portbitmap														

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
PFM	New IP checksum								M	MT-ModId		T	TGID	Mod opcode	c

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
U	Untagged Portbitmap / Src Port Number (bit0..5)														

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
Rsvd		Matched Filter		Vlan Id						Src Port			Remote Port		

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
CPU Opcodes									TimeStamp						

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
R	L3 Port Bitmap														

Fig. 5

Side Band Channel Messages

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
Opcode			Dest Port / Destination Dev Id			Src Port			DataLen			E	EC ode	Cos	C
Address															
Data															

Fig. 6

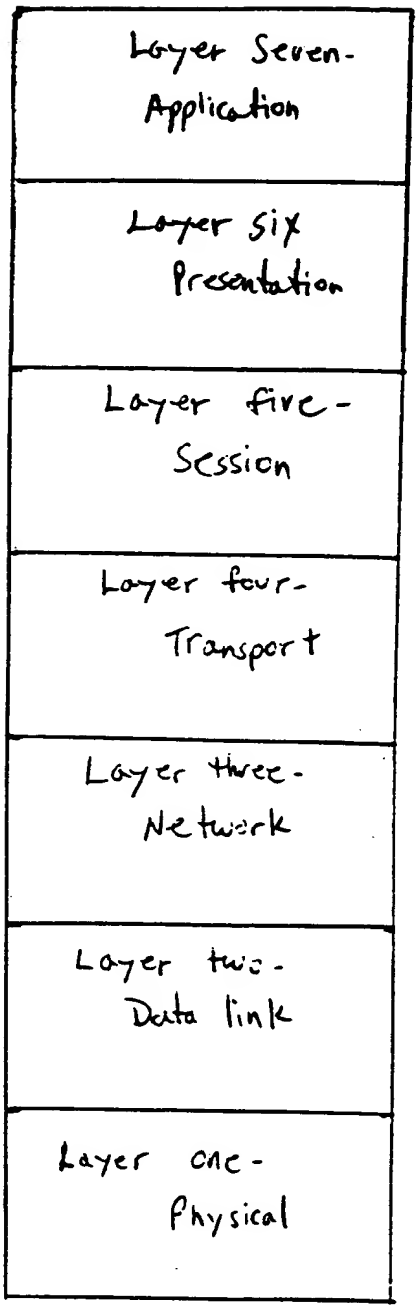


Figure 7
Prior Art

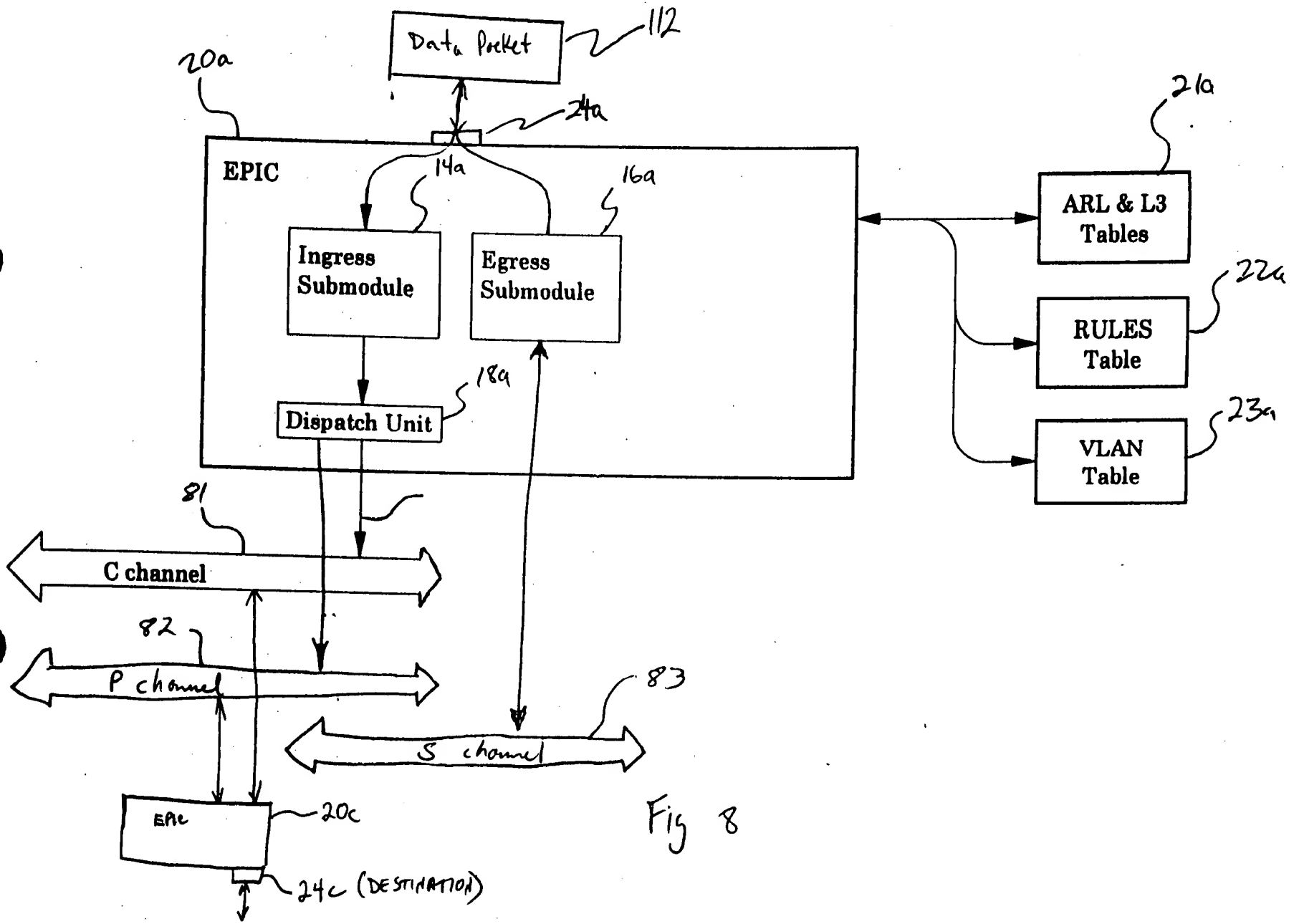

Data Packet
112
20a
24a
EPIC
14a
16a
Ingress Submodule
Egress Submodule
18a
Dispatch Unit
21a
ARL & L3 Tables
22a
RULES Table
23a
VLAN Table
81
C channel
82
P channel
83
S channel
EPIC
20c
24c (DESTINATION)

Fig 8

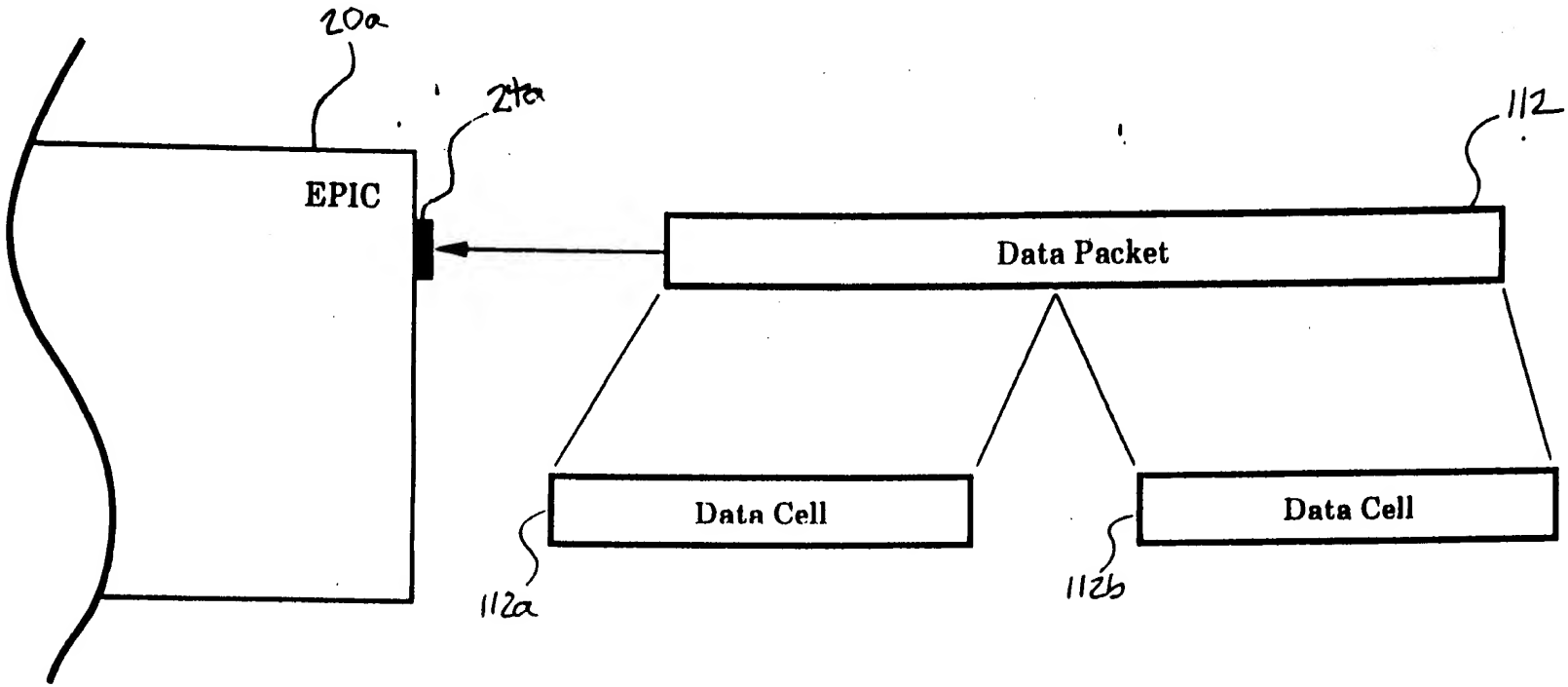


Fig. 9

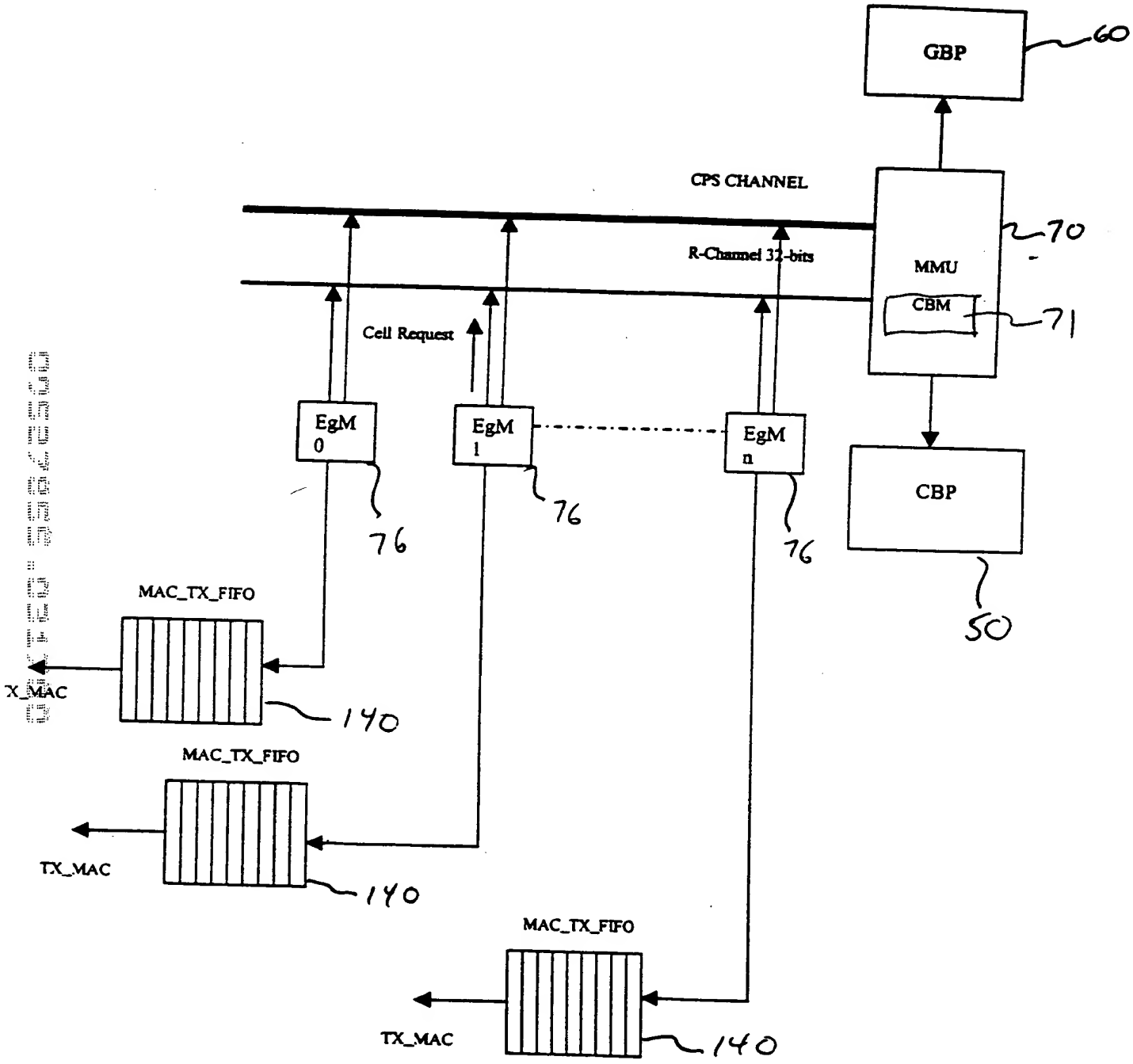

GBP
60
CPS CHANNEL
R-Channel 32-bits
MMU
70
CBM
71
Cell Request
EgM 0
EgM 1
EgM n
76
76
76
CBP
50
MAC_TX_FIFO
140
X_MAC
MAC_TX_FIFO
140
TX_MAC
MAC_TX_FIFO
140
TX_MAC

Fig. 10

Line 0 →	FC | LC | BC/MC | Cpy_cnt(5b) | Cell_length (7b) | CRC (2b) | NC_header (16b) | Src Count(6)|IPX|IP|Time_Stamp (14b) | O bits(2b) | P| NextCellLen(2b)|CpuOpcode(4b)| Cell_data (0-9B)
Line 1 →	Cell_data (10-27) Bytes
Line 2 →	Cell_data (28-45) Bytes
Line 3 →	Cell_data (46-63) Bytes

Fig. 11

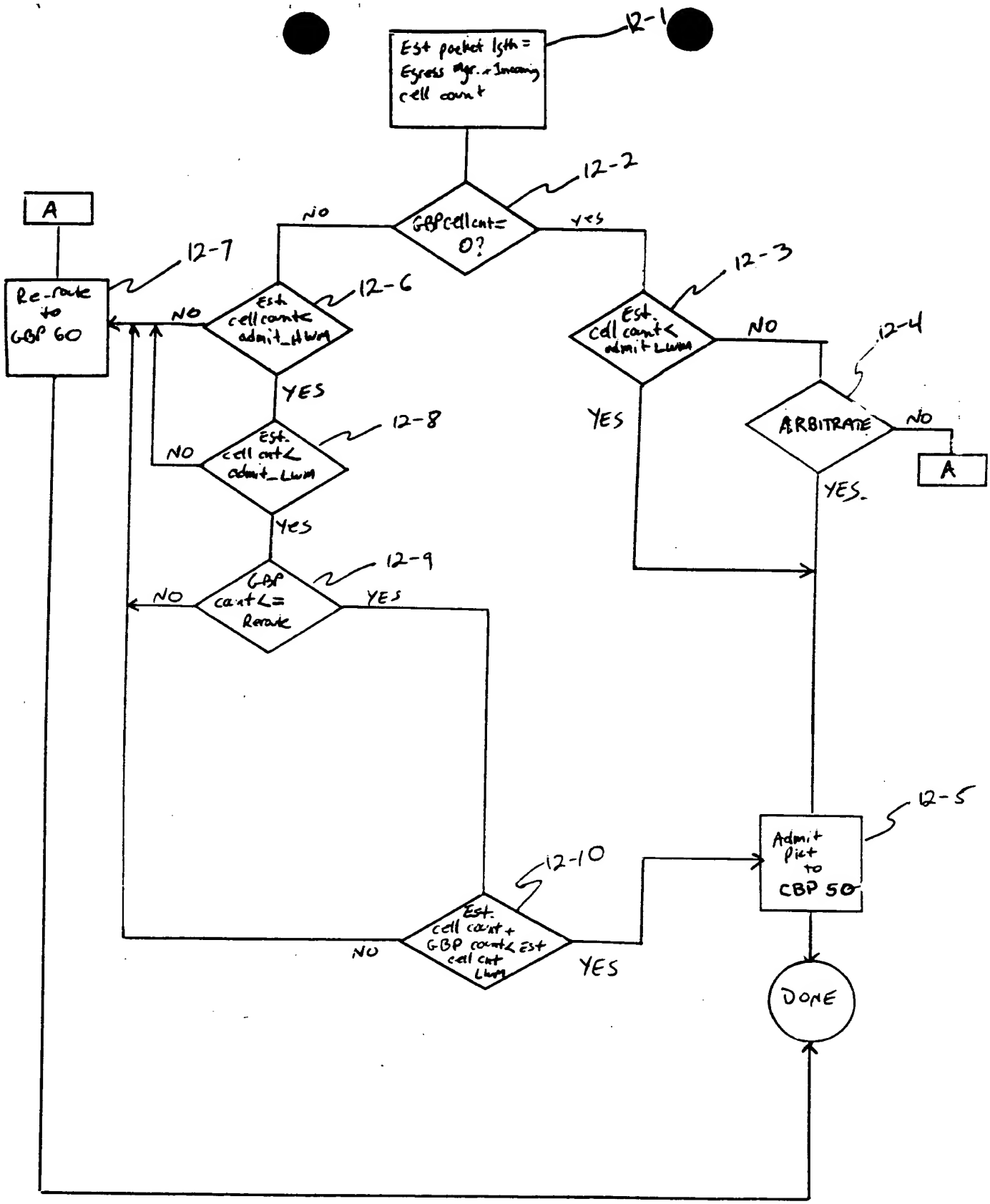

Est packet lgth = Egress mgr. - Incoming cell count
12-1
GBP cell cnt = 0?
12-2
NO
YES
A
Re-route to GBP 60
12-7
Est cell cnt < admit_HWM
12-6
NO
YES
Est. cell cnt < admit_LWM
12-3
NO
YES
ARBITRATE
12-4
NO
A
YES
Est. cell cnt < admit_LWM
12-8
NO
Yes
GBP cnt <= Remote
12-9
NO
YES
Admit pkt to CBP 50
12-5
Est. cell cnt + GBP cnt < Est cell cnt LWM
12-10
NO
YES
DONE

Fig. 12

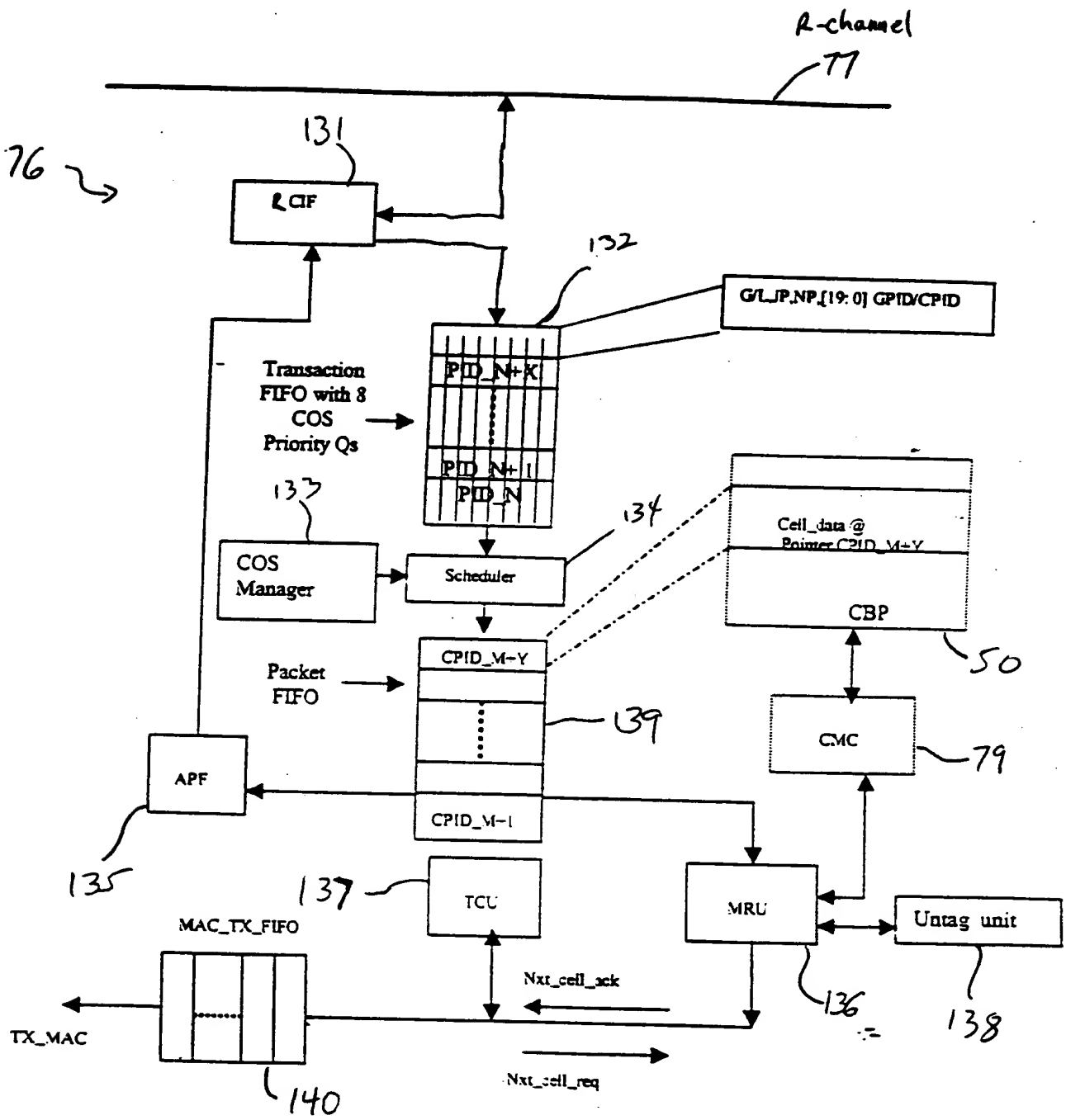

R-channel
77
76
131
RCIF
132
G/L_IP,NP,[19:0] GPID/CPID
Transaction FIFO with 8 COS Priority Qs
PID_N+K
PID_N+1
PID_N
133
COS Manager
Scheduler
134
Cell_data @ Pointer CPID_M+Y
CBP
50
Packet FIFO
CPID_M-Y
139
CPID_M-1
CMC
79
APF
135
137
TCU
MRU
Untag unit
MAC_TX_FIFO
Nxt_cell_ack
136
138
TX_MAC
140
Nxt_cell_req

Fig 13

Data Flow

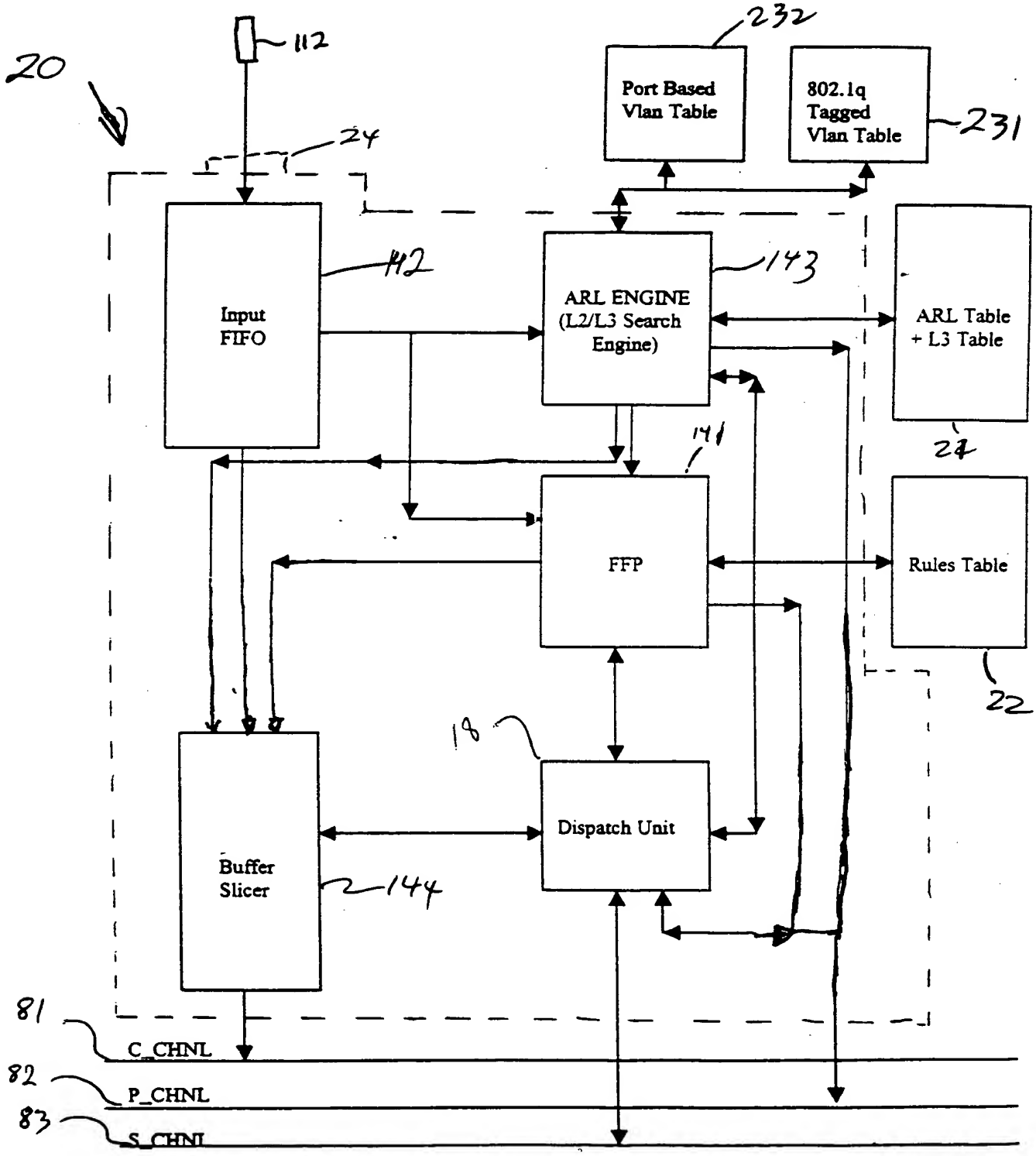


Fig. 14

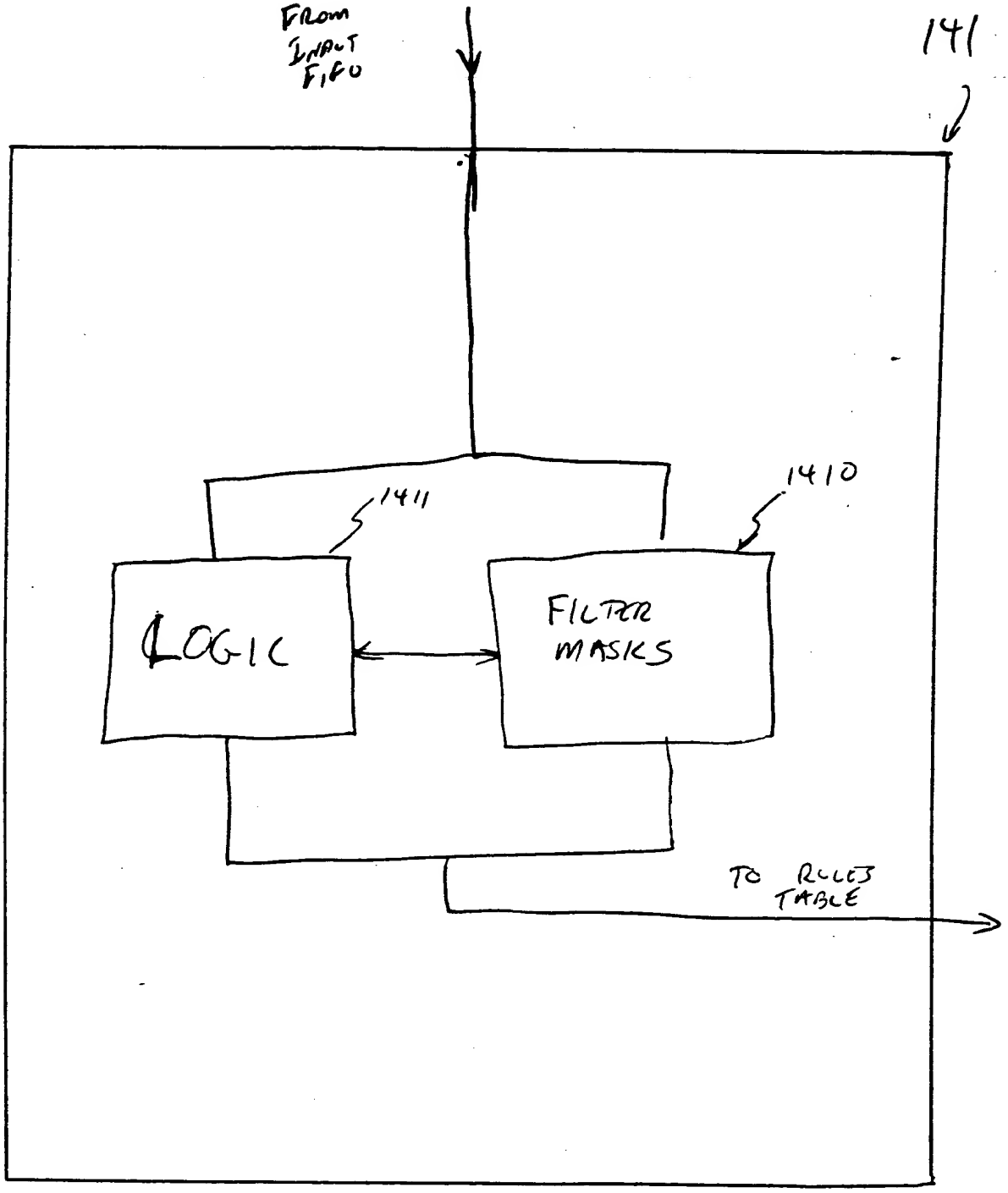

FROM INPUT FIFO
141
1411
1410
LOGIC
FILTER MASKS
TO RULES TABLE

FIG. 15

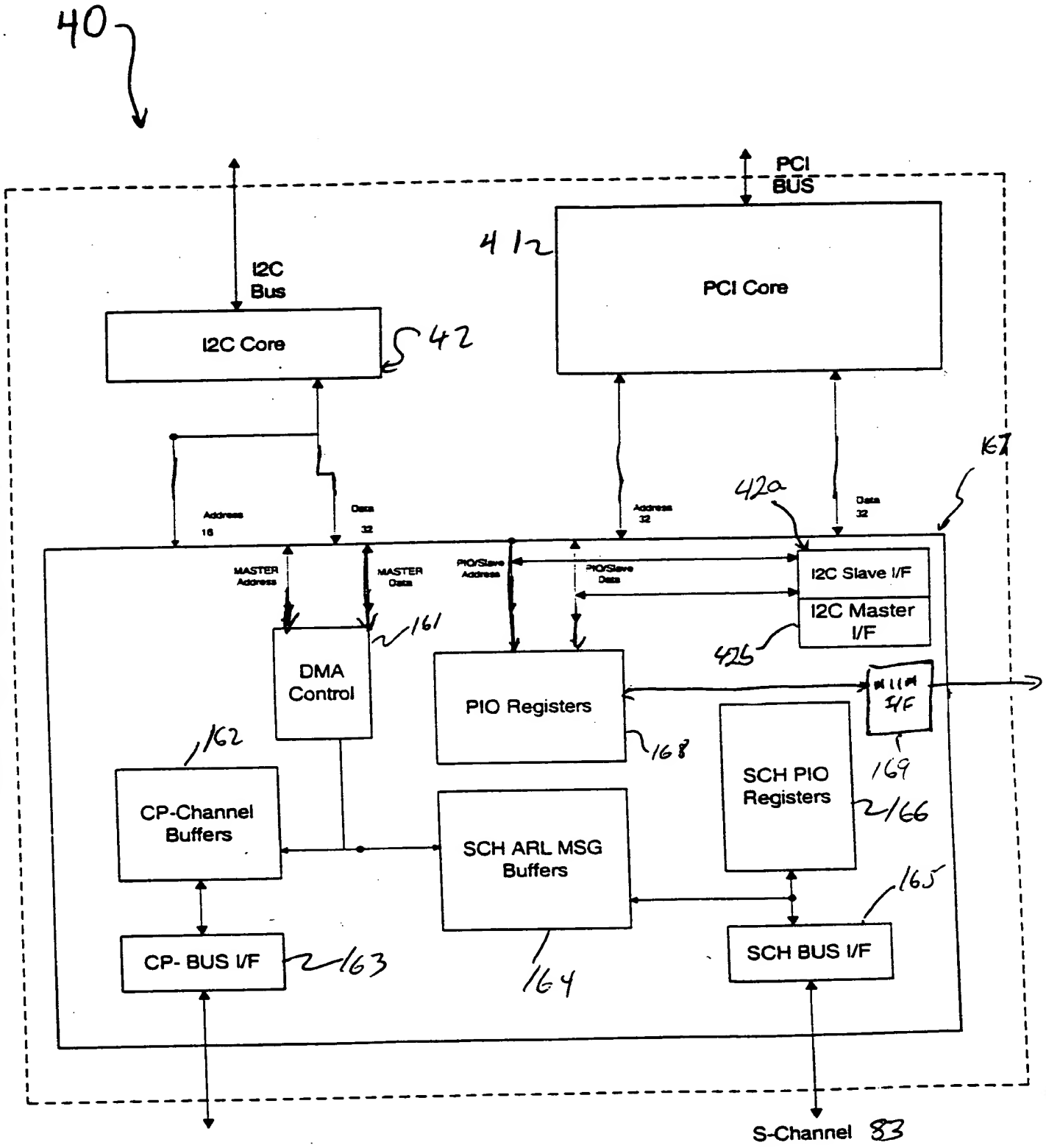

40
PCI BUS
412
PCI Core
I2C Bus
I2C Core
42
Address 16
Data 32
Address 32
Data 32
42a
I2C Slave I/F
I2C Master I/F
425
MASTER Address
MASTER Data
PIO/Slave Address
PIO/Slave Data
161
DMA Control
PIO Registers
168
162
CP-Channel Buffers
SCH ARL MSG Buffers
SCH PIO Registers
166
169
165
CP- BUS I/F
163
SCH BUS I/F
164
S-Channel 83
C channel 81
P channel 82

Fig. 16

FFP Programming Flow Chart

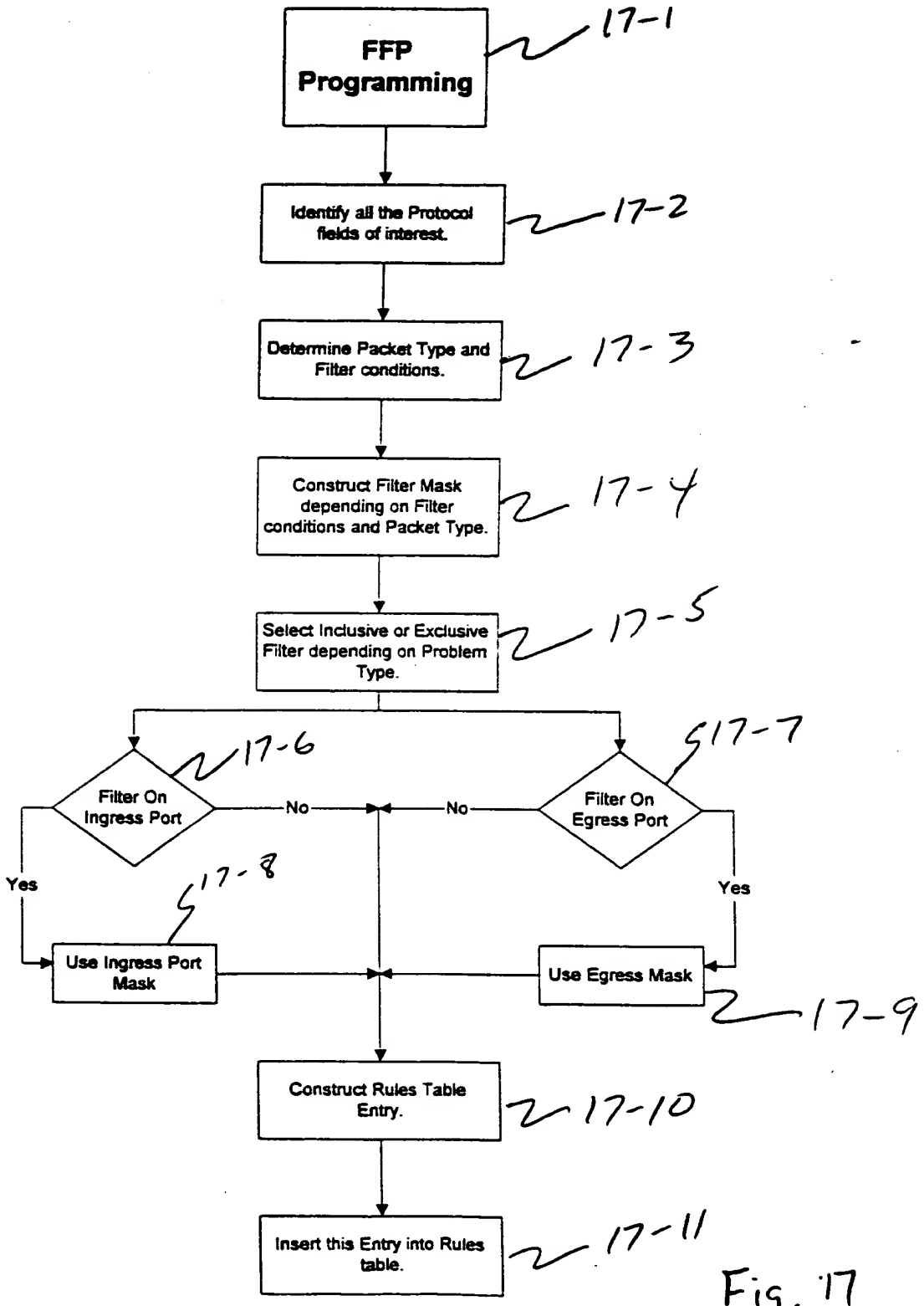


Fig. 17

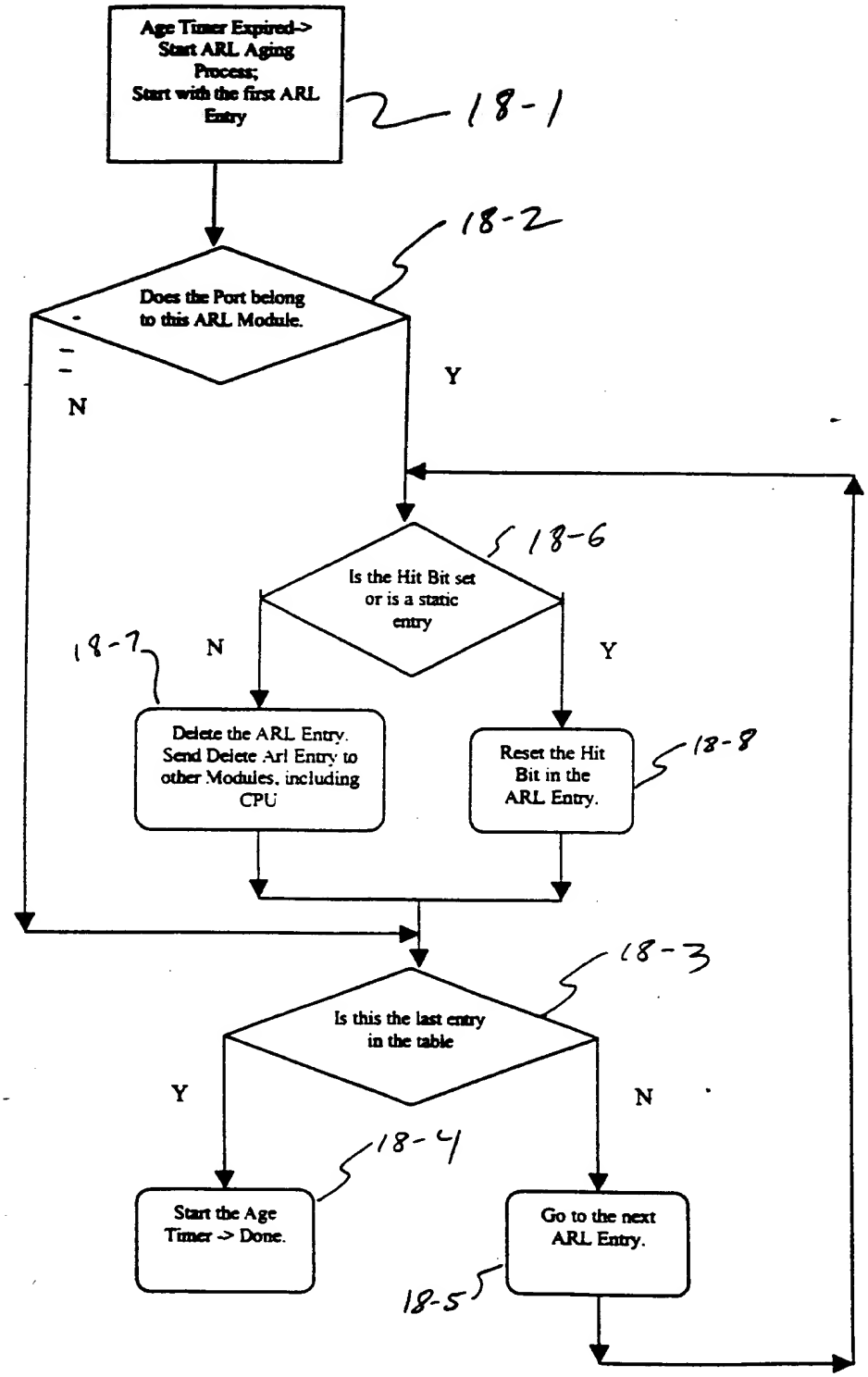

Age Timer Expired-> Start ARL Aging Process; Start with the first ARL Entry
18-1
Does the Port belong to this ARL Module.
18-2
N
Y
Is the Hit Bit set or is a static entry
18-6
N
Y
18-7
Delete the ARL Entry. Send Delete Arl Entry to other Modules, including CPU
Reset the Hit Bit in the ARL Entry.
18-8
Is this the last entry in the table
18-3
Y
N
Start the Age Timer -> Done.
18-4
Go to the next ARL Entry.
18-5

Fig. 18

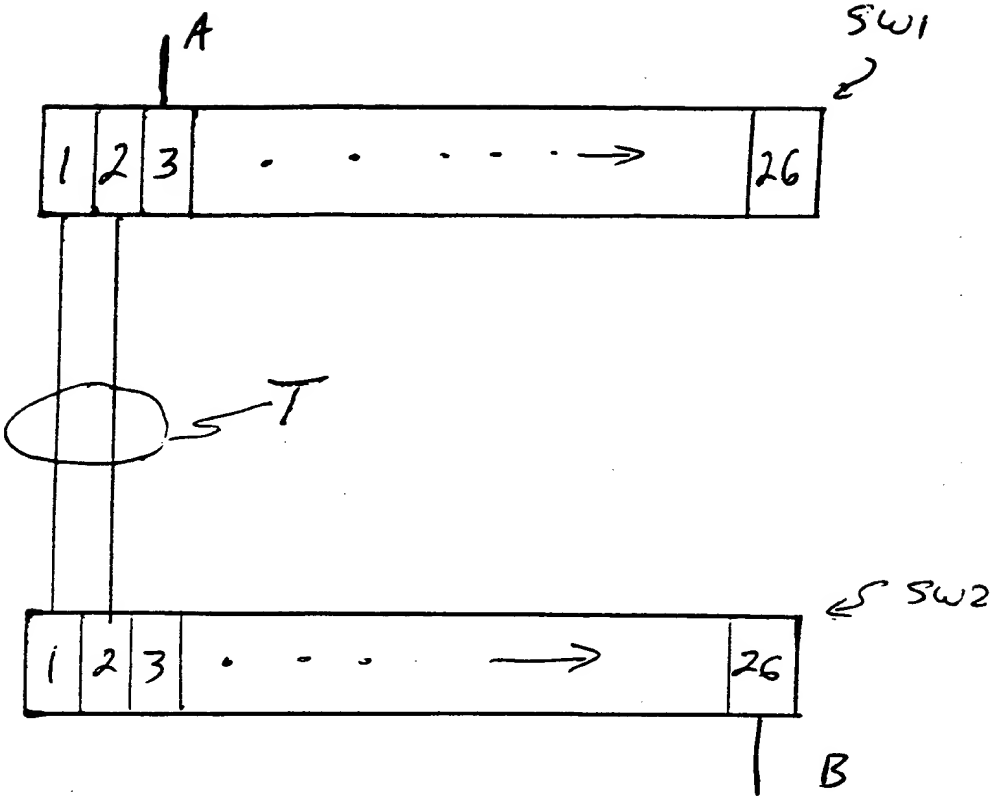


Fig. 19

Field	Header	Size	Offset For Ethernet II Untagged	Offset For Ethernet II Tagged	Offset For SNAP Untagged	Offset For SNAP Tagged
Destination Mac Address	Mac	6 Bytes	0	0	0	0
Source Mac Address	Mac	6 Bytes	6	6	6	6
Protocol Type	Mac	2 Bytes	12	16	20	24
Destination SAP	802.3	1 Byte	NA	NA	14	18
Source SAP	802.3	1 Byte	NA	NA	15	19
802.1p Priority	Mac	3 bits	NA	14	NA	14
VLAN Id	Mac	12 bits	NA	14+ 4b	NA	14+4b
TOS Precedence	IP	3 bits	15	19	23	27
Differentiated Services	IP	6 bits	15	19	23	27
Source IP Address	IP	4 Bytes	26	30	34	38
Destination IP Address	IP	4 Bytes	30	34	38	42
Protocol	IP	1 Byte	23	27	31	35
Source Port	TCP/ UDP	2 Bytes	34	38	42	46
Destination Port	TCP/ UDP	2 Bytes	36	40	44	48
TCP Control Flags (For aligning on Byte boundary 2 bits of reserved bits preceding this field is included)	TCP	1 Byte	47	51	55	59
Data at Offset 1	NA	8 Bytes	Data Offset1 From start of IP / IPX Header	Data Offset1 From start of IP / IPX Header	Data Offset1 From start of IP / IPX Header	Data Offset1 From start of IP / IPX Header
Data at Offset 2	NA	8 Bytes	Data Offset2 From start of IP / IPX Header	Data Offset2 From start of IP /IPX Header	Data Offset2 From start of IP / IPX Header	Data Offset2 From start of IP / IPX Header
Data at Offset 3	NA	8 Bytes	Data Offset3 From start of IP / IPX Header	Data Offset3 From start of IP / IPX Header	Data Offset3 From start of IP / IPX Header	Data Offset3 From start of IP / IPX Header
Data at Offset 4	NA	8 Bytes	Data Offset4 From start of IP /IPX Header	Data Offset4 From start of IP / IPX Header	Data Offset4 From start of IP / IPX Header	Data Offset4 From start of IP / IPX Header

FIGURE 20

Fig. 21a

Filter Mask Format:

Filter Enable (1b)	Counter (5b)	Rem Port (1b)	Output Mod (5b)	Output Port (6b)	TOS Prec (3b)	Diff Serv (6b)	802.1p Prior (3b)

NMA Enb (1b)	No Match Action (10b)	Data Offset 4 (7b)	Data Offset 3 (7b)	Data Offset 2 (7b)	Data Offset 1 (7b)	Ingress Port Mask (6b)	Egress ModId Mask (5b)	Egress Port Mask (6b)
Field Mask								

Fig 21b.

Field Mask Format:

Dest Mac addr (6 B)	Src Mac addr (6 B)	Prot type (2 B)	Dest SAP (1 B)	Src SAP (1 B)	802.1 p Prio (3 b)	Vlan Id (12b)	TOS Prec (3b)	Diff Serv (6b)	Src IP addr (4B)	Dest IP addr (4 B)	Prot IP- (1B)	Src Port (2B)	Dest Port (2B)

TCP Cntr Flags (1B)	Data 1 (8B)	Data 2 (8 B)	Data 3 (8B)	Data 4 (8B)

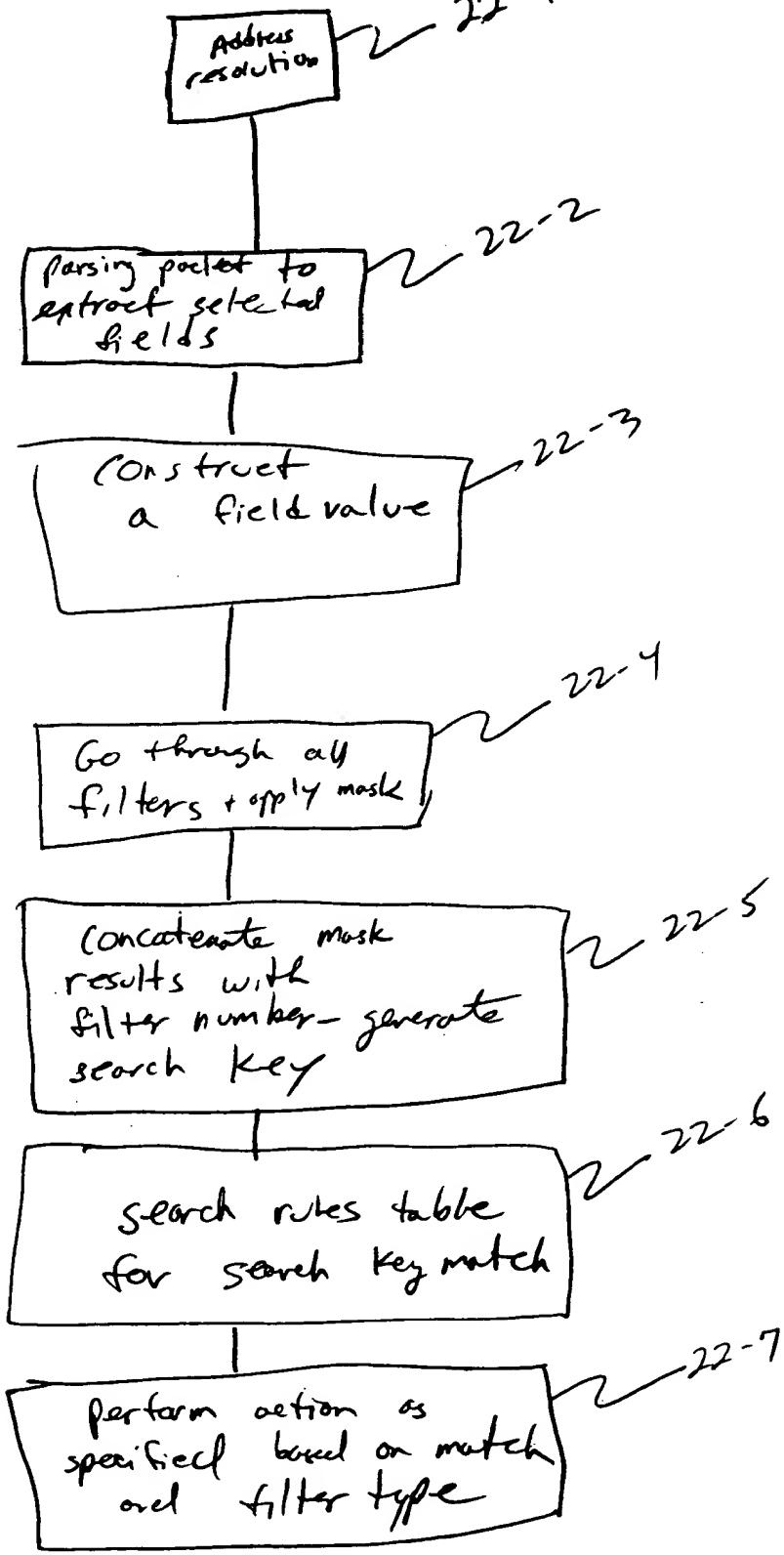

Address resolution
22-1
Parsing packet to extract selected fields
22-2
Construct a field value
22-3
Go through all filters + apply mask
22-4
Concatenate mask results with filter number - generate search key
22-5
search rules table for search key match
22-6
perform action as specified based on matched filter type
22-7

Fig. 22

22

Counter (5b)	Output Mod (5b)	Output Port (6b)	TOS_P (3b)	Diff Services (6b)	802.1p Priority (3b)	Actions (11b)	Filter Select (3b)	Ingress Port (6b)	Egrs Mod (5b)	Egrs Port (6b)	Filter Value (512 b)

Fig. 23

802.1p/Q VLAN Tagging

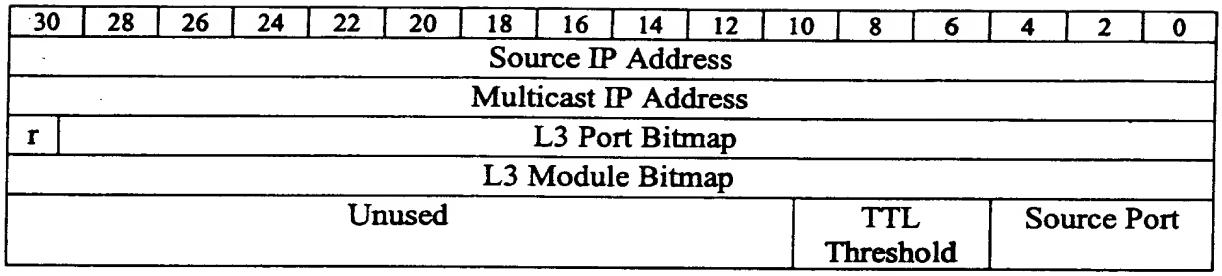


Fig. 24

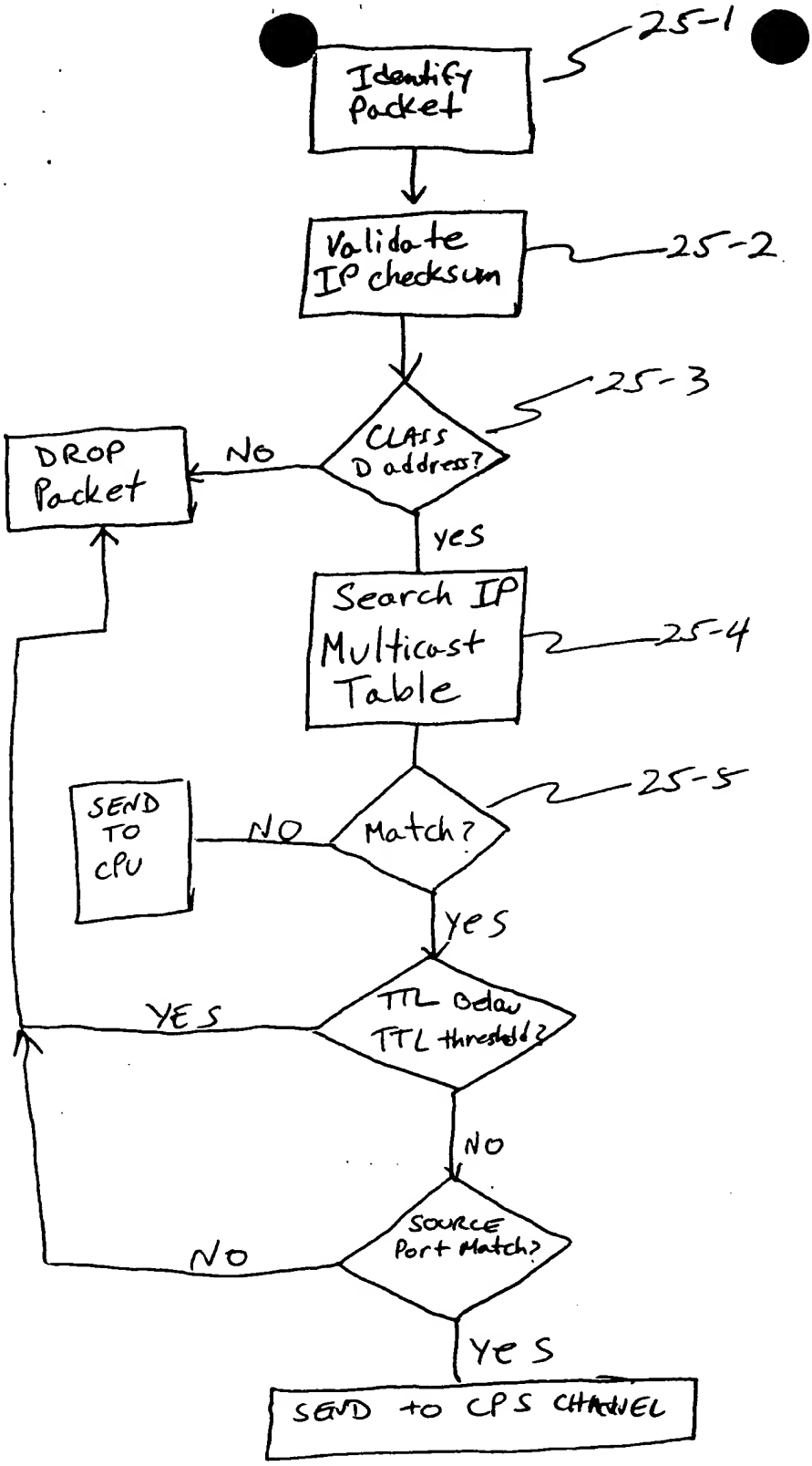

Identify Packet
25-1
Validate IP checksum
25-2
CLASS D address?
25-3
NO
DROP Packet
yes
Search IP Multicast Table
25-4
Match?
25-5
NO
SEND TO CPU
yes
TTL Odaw TTL threshold?
YES
NO
SOURCE Port Match?
NO
Yes
SEND to CPS CHANNEL

Fig. 25

CPU
52
CMIC
40(2)
CMIC
40(1)
SOC
SOC
IPIC
IPIC
26
90(2)
90(1)
24(2)
10(2)
10(1)
24(1)

Fig. 2G

Fig. 27a

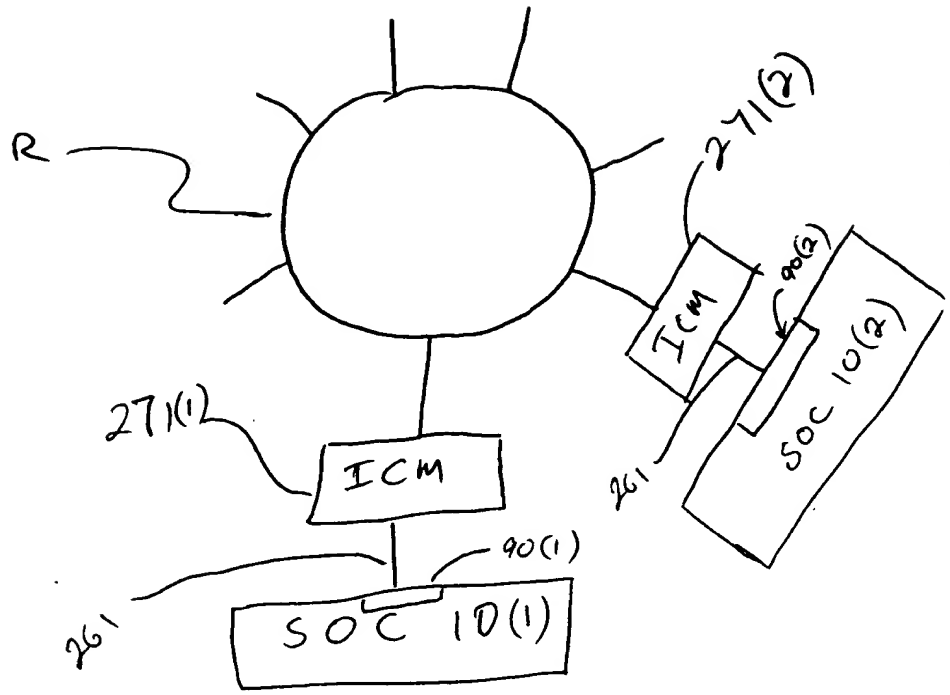
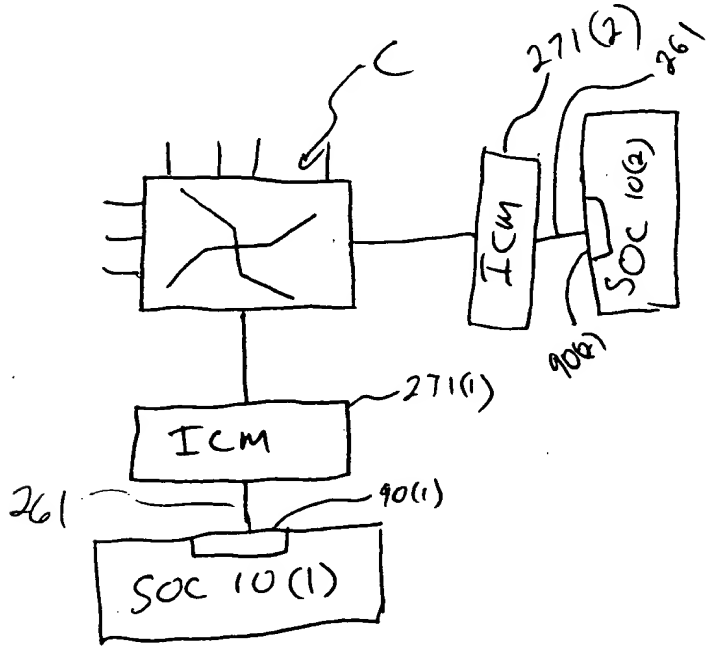


Fig. 27b



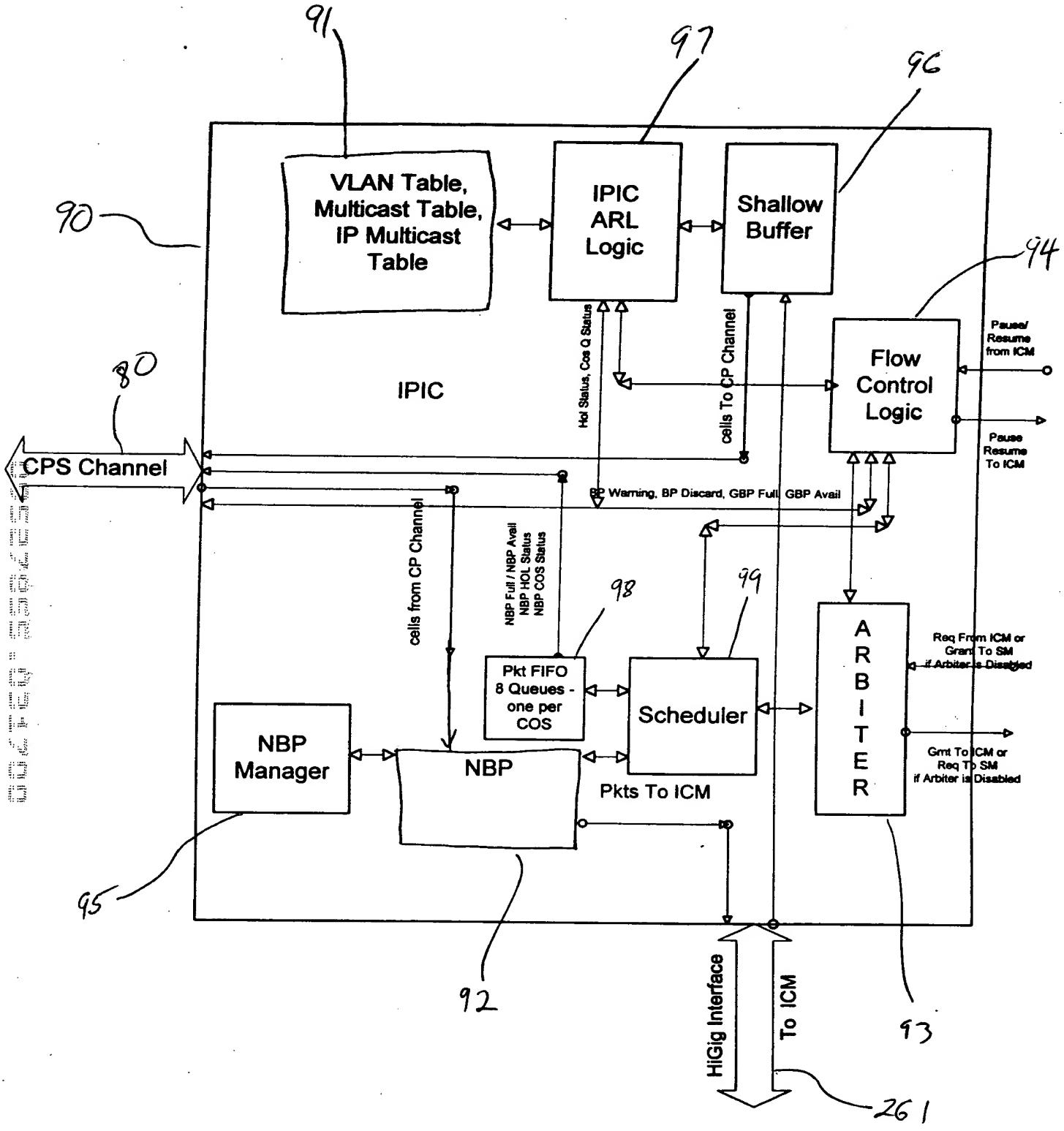

91
97
96
VLAN Table, Multicast Table, IP Multicast Table
IPIC ARL Logic
Shallow Buffer
90
94
80
IPIC
Hol Status, Cos Q Status
cells To CP Channel
Flow Control Logic
Pause/ Resume from ICM
Pause Resume To ICM
CPS Channel
BP Warning, BP Discard, GBP Full, GBP Avail
cells from CP Channel
NBP Full / NBP Avail
NBP HOL Status
NBP COS Status
98
99
Pkt FIFO 8 Queues - one per COS
Scheduler
ARBITER
Req From ICM or Grant To SM if Arbiter is Disabled
Grnt To ICM or Req To SM if Arbiter is Disabled
NBP Manager
NBP
Pkts To ICM
95
92
93
HiGig Interface
To ICM
261

Fig 28.

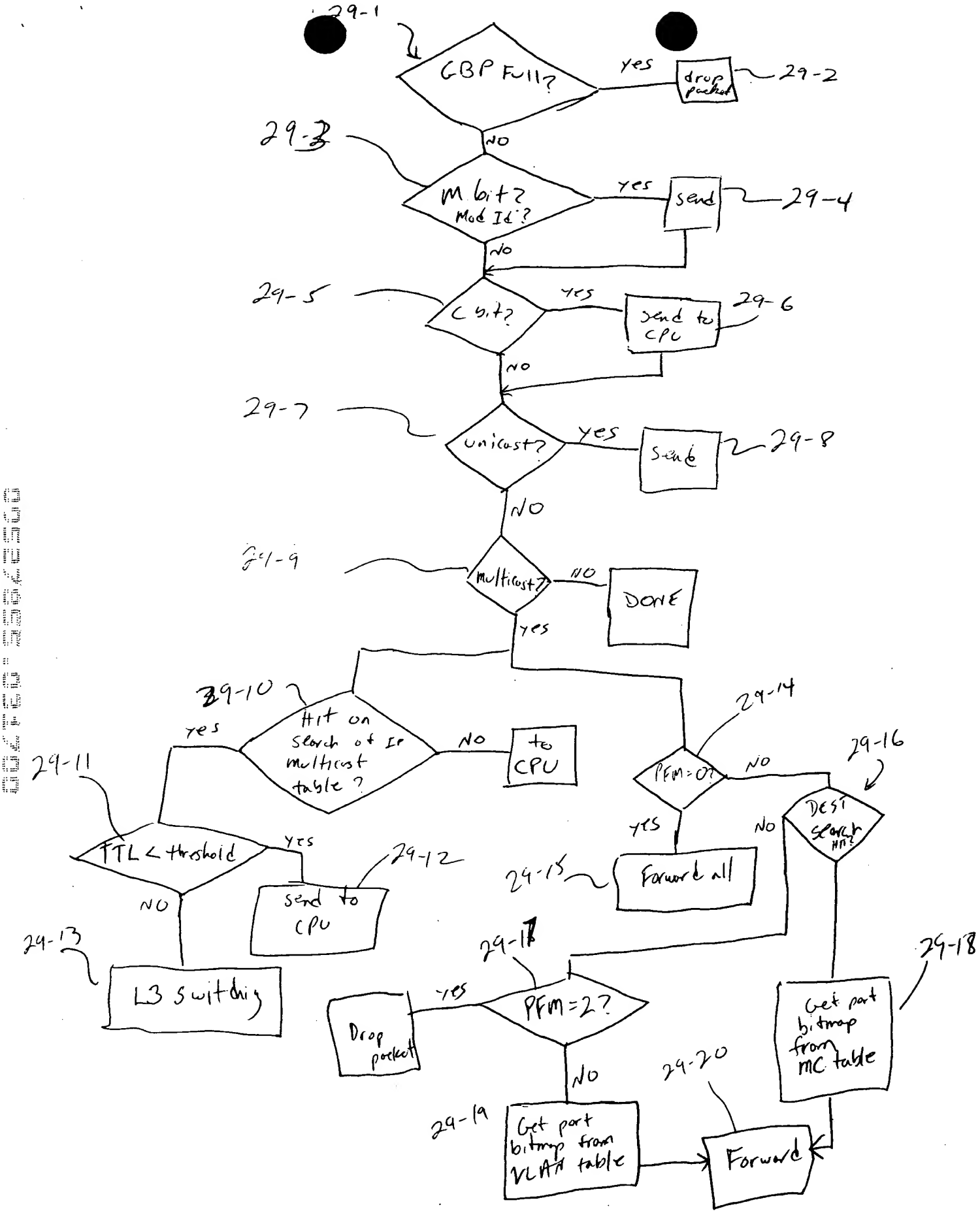

29-1
CBP Full?
yes
drop packet
29-2
29-3
M bit? Mod Id?
yes
send
29-4
no
29-5
C bit?
yes
send to CPU
29-6
no
29-7
unicast?
yes
send
29-8
no
29-9
multicast?
no
DONE
yes
29-10
Hit on search of IP multicast table?
yes
no
to CPU
29-11
TTL < threshold
yes
29-12
send to CPU
no
29-13
L3 switching
29-14
PFM=0?
no
29-16
Dest search hit?
yes
29-15
Forward all
no
29-17
PFM=2?
yes
Drop packet
no
29-18
Get port bitmap from MC table
29-19
Get port bitmap from VLAN table
29-20
Forward

Fig. 29

COS Queue (3b)	C P F	NCA (2b)	802.1p Priority (3b)	Rate Counter (8b)	Rate Counter Threshold (8b)	Rate Discard Threshold (8b)	New Code Point (6b)	New COS Queue (3b)	New 802.1 Priority (3b)

FIGURE 30

Offset Field	Offset 1	Offset 2	Offset 3	Offset 4
000	0-15	16-31	32-47	48-63
001	8-23	24-39	40-55	56-71
010	16-31	32-47	48-63	64-79
011	24-39	40-55	56-71	72-87
100	32-47	48-63	64-79	80-95
101	40-55	56-71	72-87	88-103
110	48-63	64-79	80-95	96-111
111	56-71	72-87	88-103	104-119

Figure 31

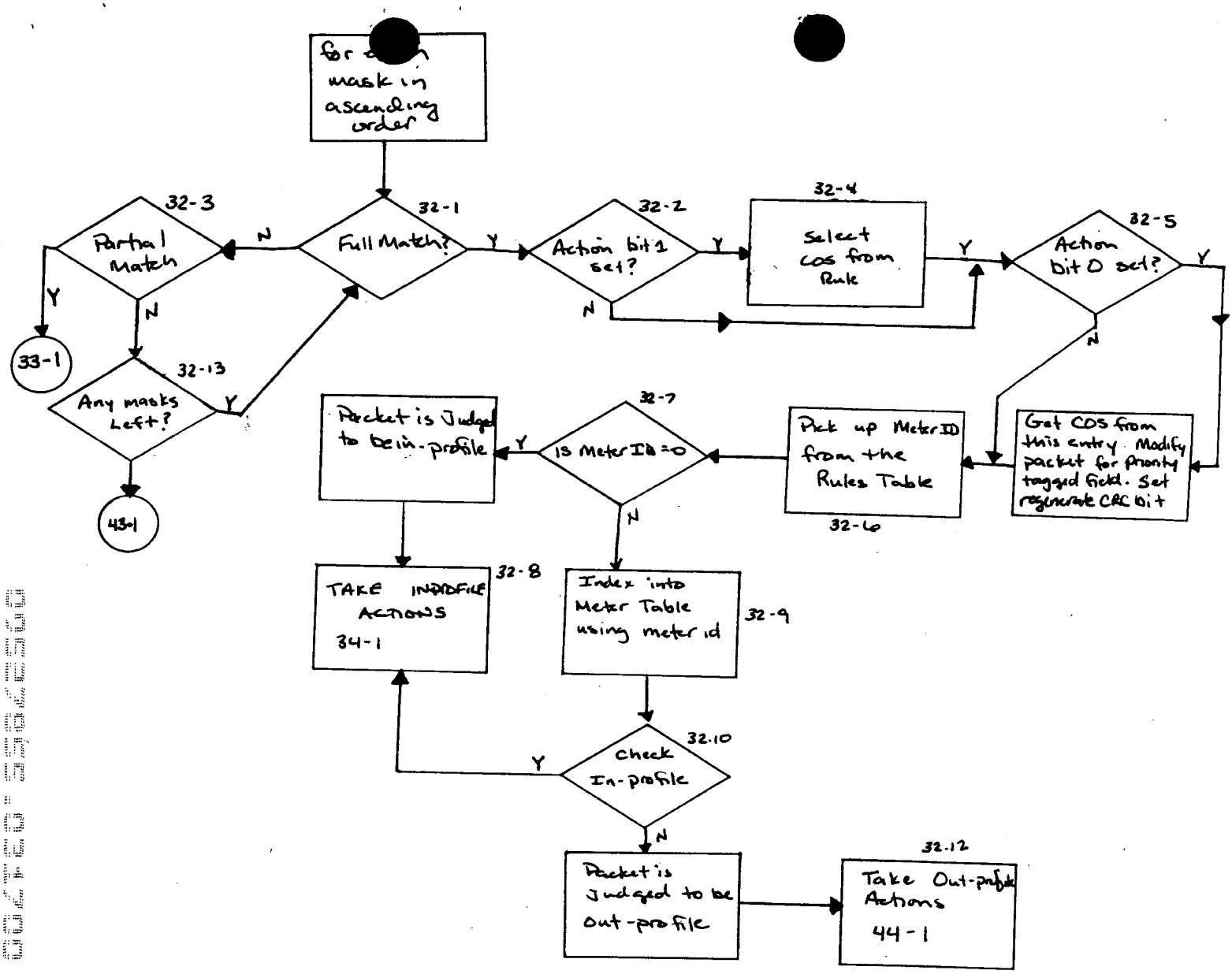

for each mask in ascending order
32-3
Partial Match
32-1
Full Match?
32-2
Action bit 1 set?
32-4
Select cos from Rule
32-5
Action bit 0 set?
33-1
32-13
Any masks Left?
43-1
Packet is Judged to be in-profile
32-7
IS MeterID=0
Pick up MeterID from the Rules Table
32-6
Get COS from this entry. Modify packet for priority tagged field. Set regenerate CRC bit
32-8
TAKE INPROFILE ACTIONS
34-1
Index into Meter Table using meter id
32-9
32.10
Check In-profile
Packet is Judged to be out-profile
32.12
Take Out-profile Actions
44-1
Y
N

FIGURE 32

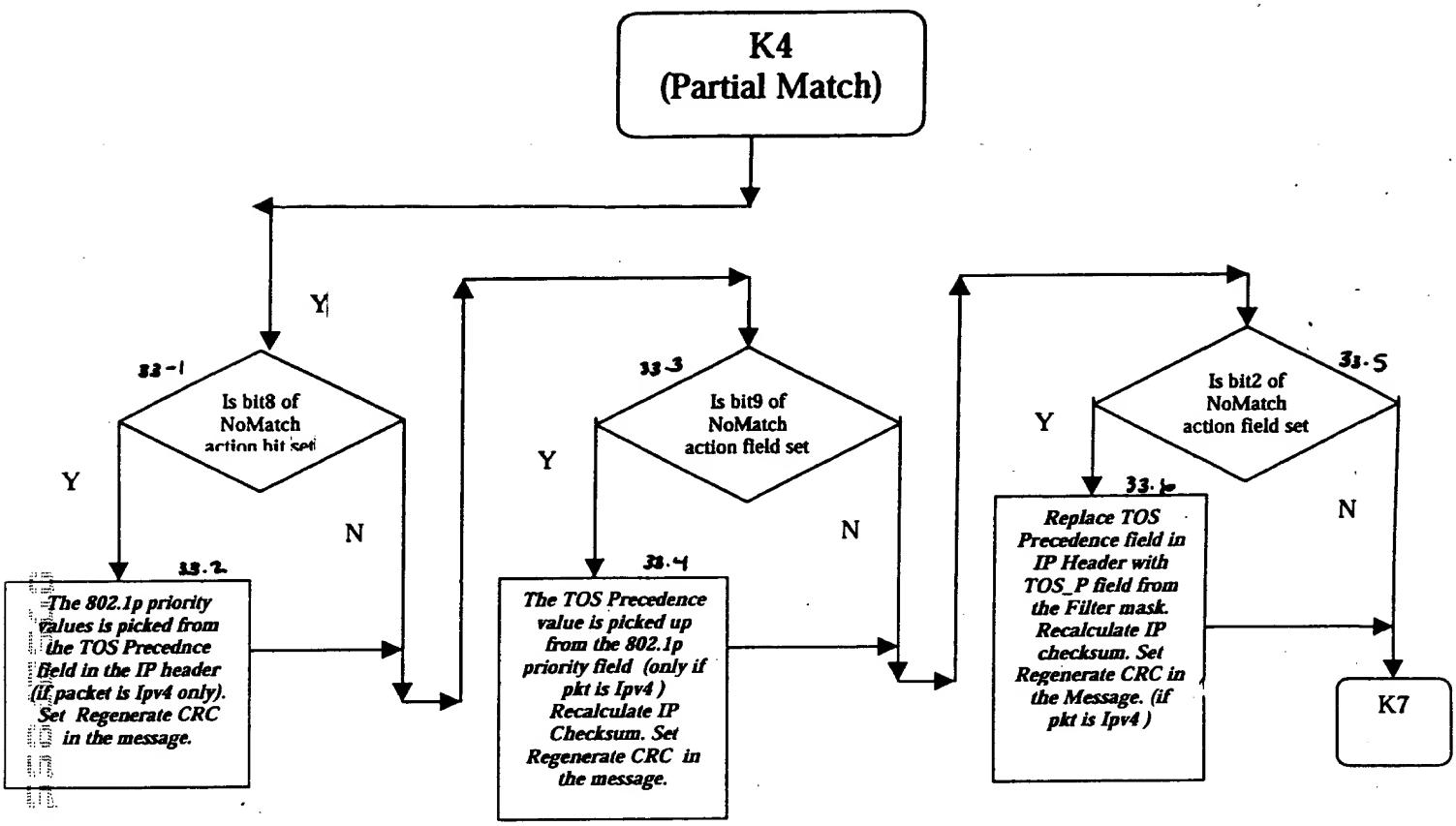

K4
(Partial Match)
Y
33-1
Is bit8 of
NoMatch
action bit set
Y
N
33.2
The 802.1p priority values is picked from the TOS Precednce field in the IP header (if packet is Ipv4 only). Set Regenerate CRC in the message.
33.3
Is bit9 of
NoMatch
action field set
Y
N
33.4
The TOS Precedence value is picked up from the 802.1p priority field (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.
33.5
Is bit2 of
NoMatch
action field set
Y
N
33.6
Replace TOS Precedence field in IP Header with TOS_P field from the Filter mask. Recalculate IP checksum. Set Regenerate CRC in the Message. (if pkt is Ipv4)
K7

FIGURE 33

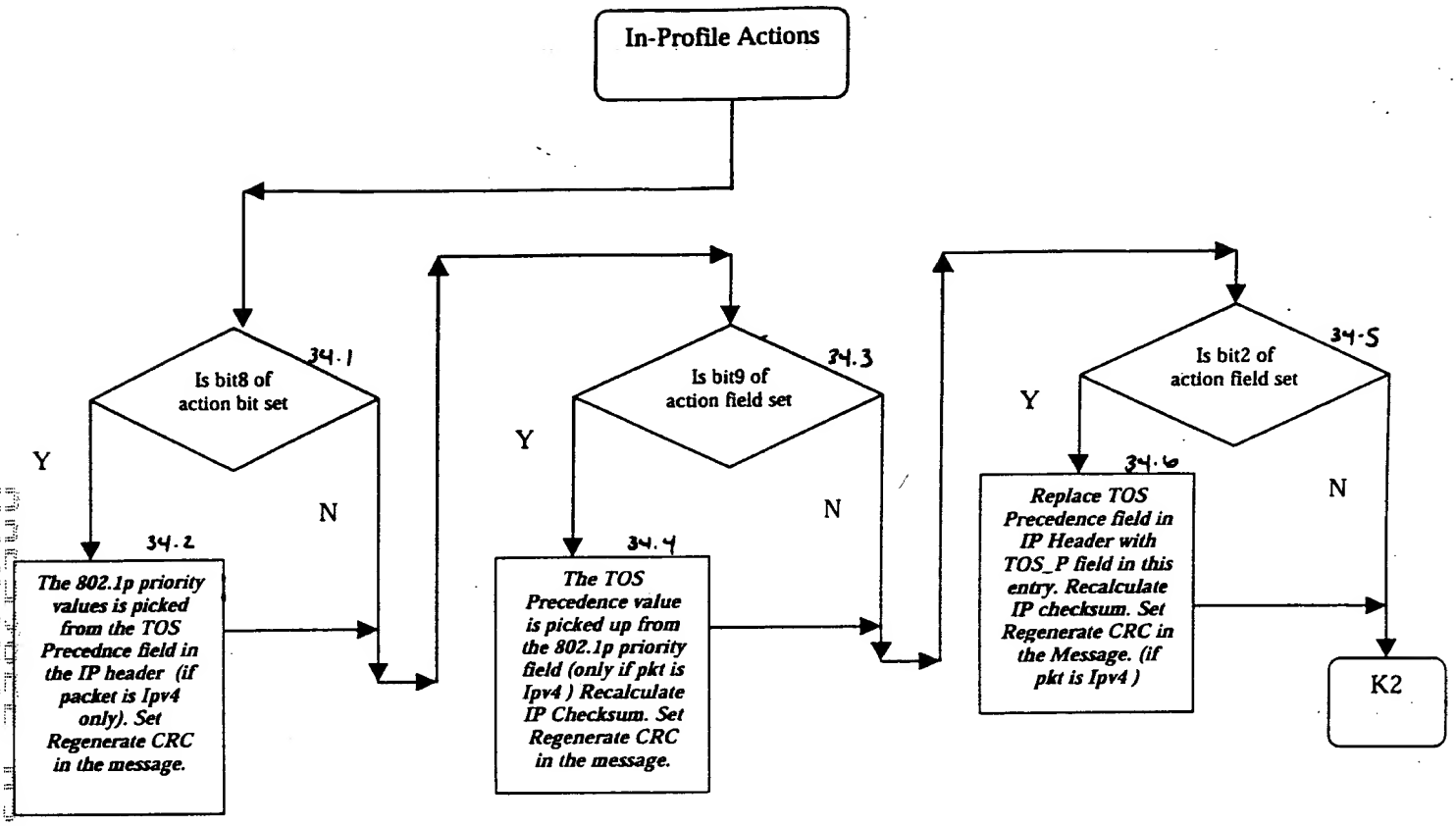

In-Profile Actions
34.1
Is bit8 of action bit set
Y
N
34.2
The 802.1p priority values is picked from the TOS Precednce field in the IP header (if packet is Ipv4 only). Set Regenerate CRC in the message.
34.3
Is bit9 of action field set
Y
N
34.4
The TOS Precedence value is picked up from the 802.1p priority field (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.
34.5
Is bit2 of action field set
Y
N
34.6
Replace TOS Precedence field in IP Header with TOS_P field in this entry. Recalculate IP checksum. Set Regenerate CRC in the Message. (if pkt is Ipv4)
K2

FIGURE 34

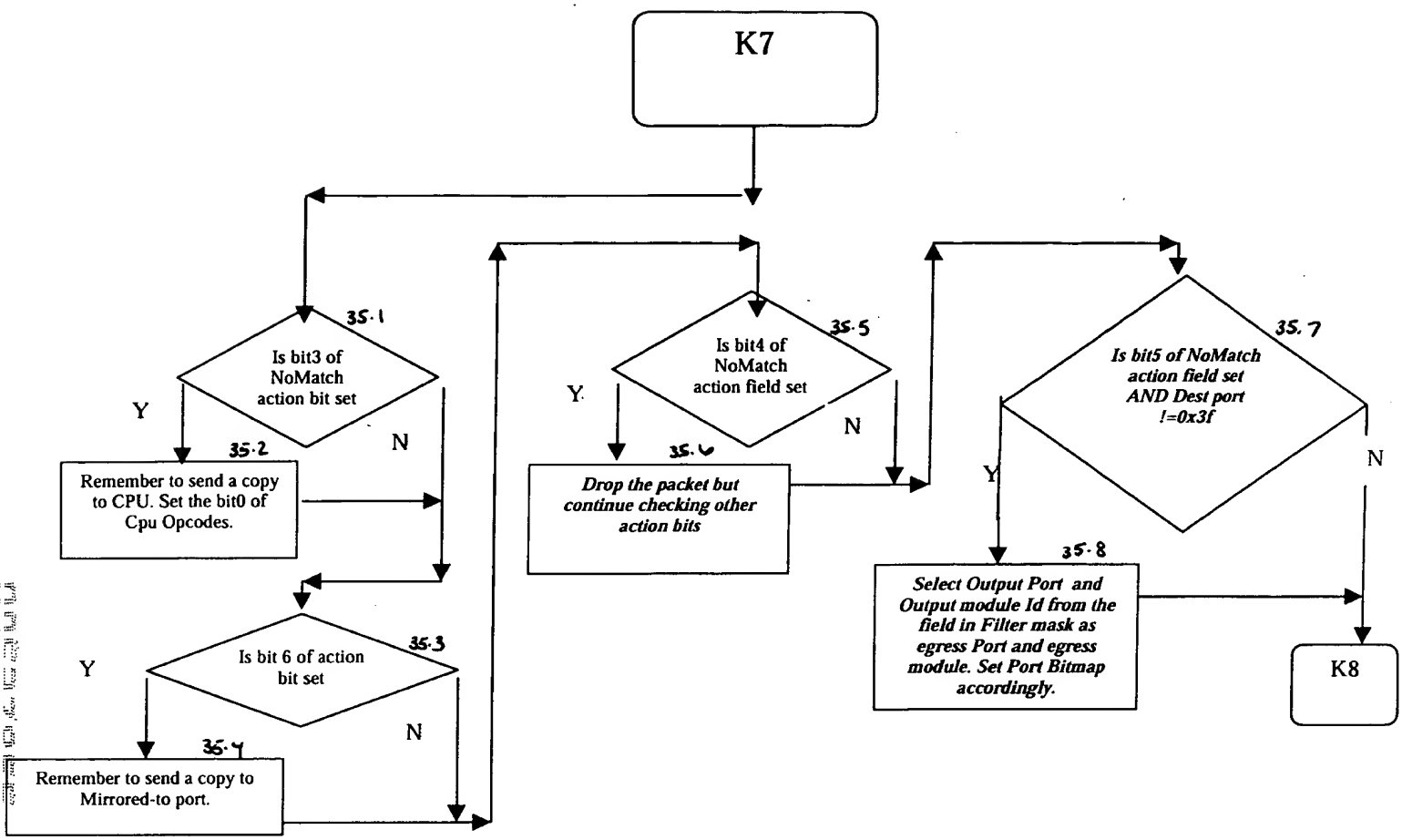

K7
35.1
Is bit3 of NoMatch action bit set
Y
N
35.2
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
35.3
Is bit 6 of action bit set
Y
N
35.4
Remember to send a copy to Mirrored-to port.
35.5
Is bit4 of NoMatch action field set
Y
N
35.6
Drop the packet but continue checking other action bits
35.7
Is bit5 of NoMatch action field set AND Dest port !=0x3f
Y
N
35.8
Select Output Port and Output module Id from the field in Filter mask as egress Port and egress module. Set Port Bitmap accordingly.
K8

FIGURE 35

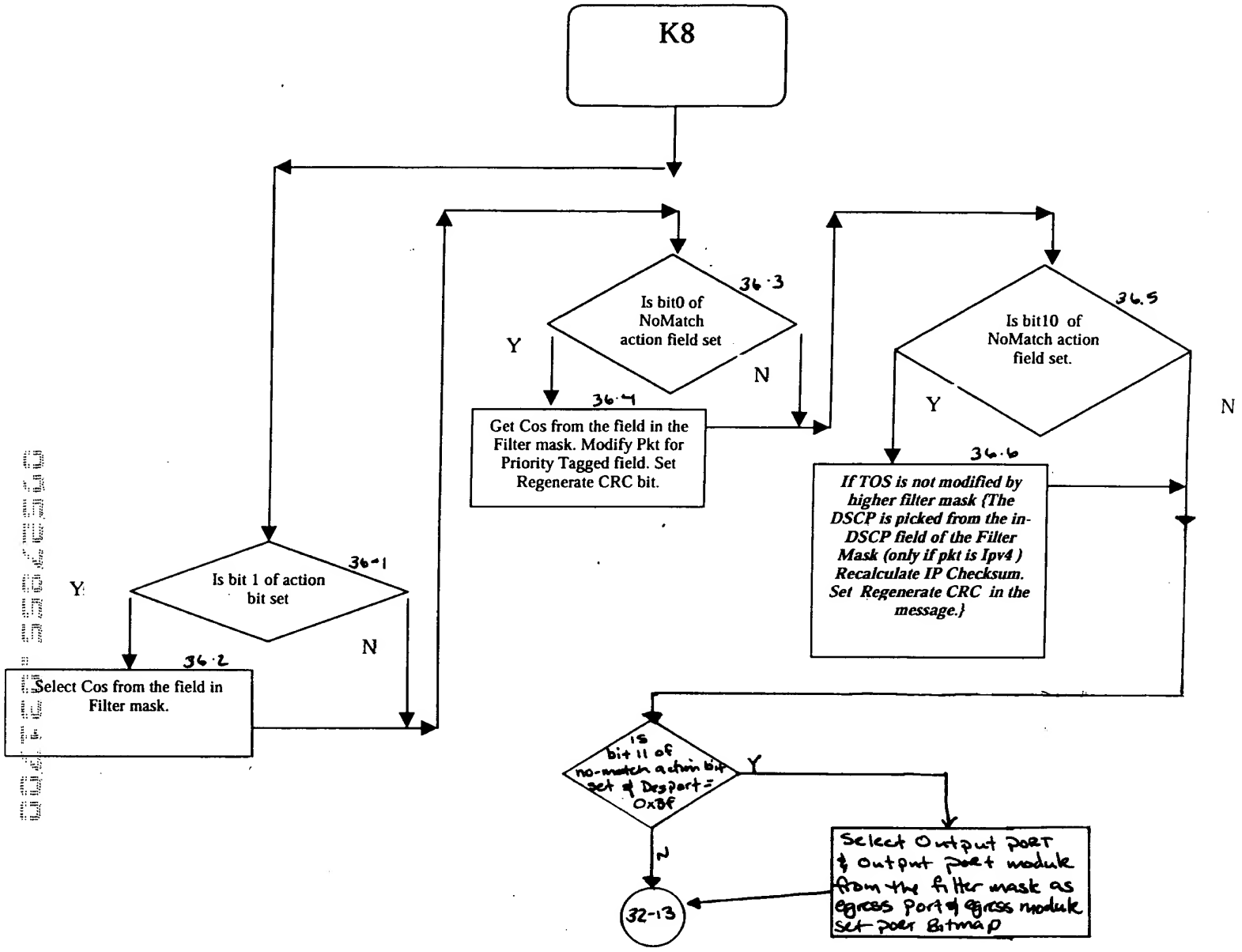

K8
36-3
Is bit0 of NoMatch action field set
Y
N
36-4
Get Cos from the field in the Filter mask. Modify Pkt for Priority Tagged field. Set Regenerate CRC bit.
36.5
Is bit10 of NoMatch action field set.
Y
N
36-6
If TOS is not modified by higher filter mask (The DSCP is picked from the in-DSCP field of the Filter Mask (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.)
36-1
Is bit 1 of action bit set
Y
N
36-2
Select Cos from the field in Filter mask.
Is bit 11 of no-match action bit set & Dest port = 0x3f
Y
N
Select Output PORT & Output port module from the filter mask as egress port & egress module set port Bitmap
32-13

FIGURE 36

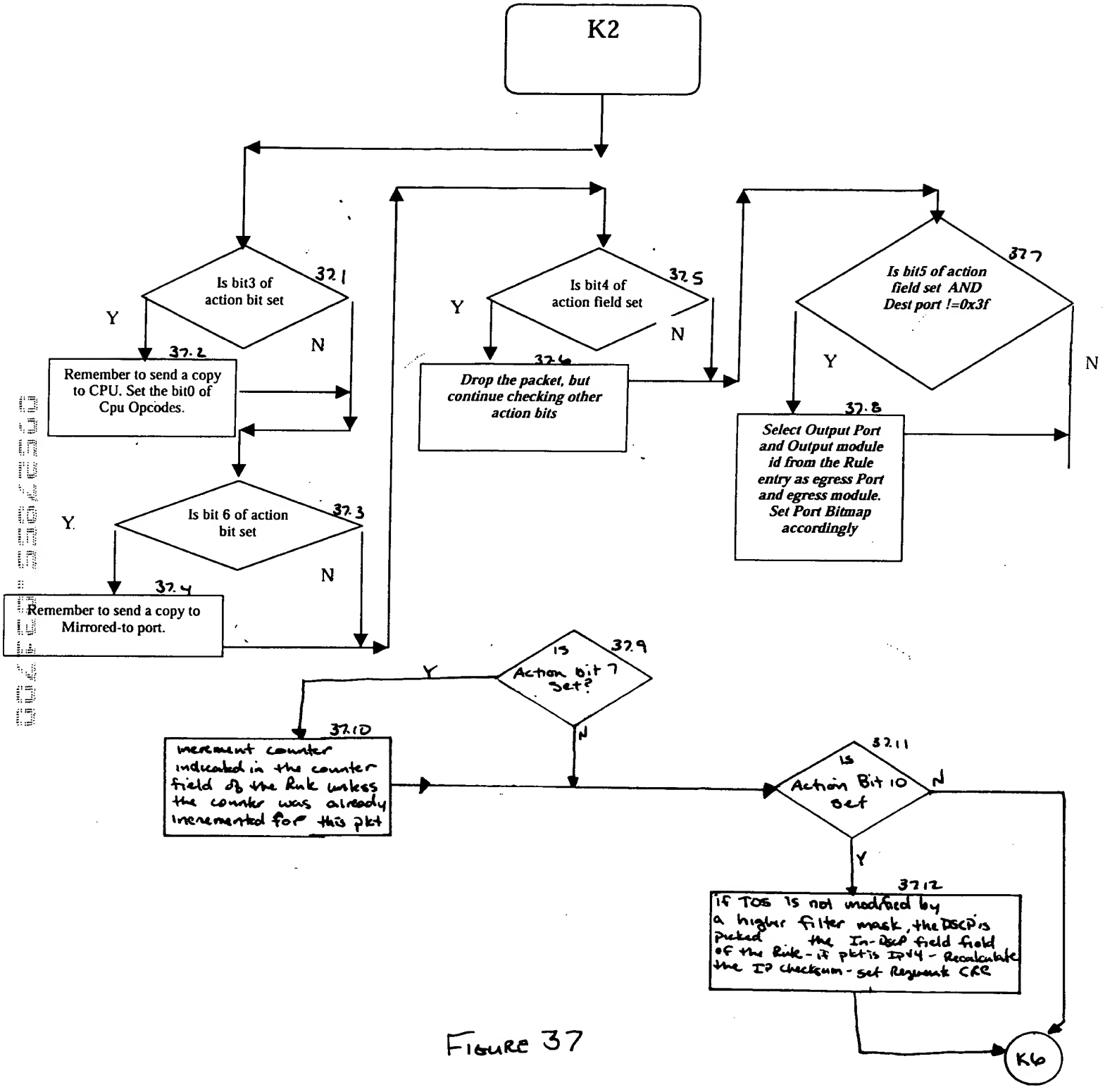

K2
Is bit3 of action bit set
321
Y
N
37.2
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
Is bit 6 of action bit set
37.3
Y
N
37.4
Remember to send a copy to Mirrored-to port.
Is bit4 of action field set
37.5
Y
N
37.6
Drop the packet, but continue checking other action bits
Is bit5 of action field set AND Dest port !=0x3f
37.7
Y
N
37.8
Select Output Port and Output module id from the Rule entry as egress Port and egress module. Set Port Bitmap accordingly
Is Action bit 7 set?
37.9
Y
N
37.10
increment counter indicated in the counter field of the Rule unless the counter was already incremented for this pkt
Is Action Bit 10 set
37.11
N
Y
37.12
if TOS is not modified by a higher filter mask, the DSCP is picked the In-DSCP field field of the Rule - if pkt is IPv4 - Recalculate the IP checksum - set Regenerate CRC
K6

FIGURE 37

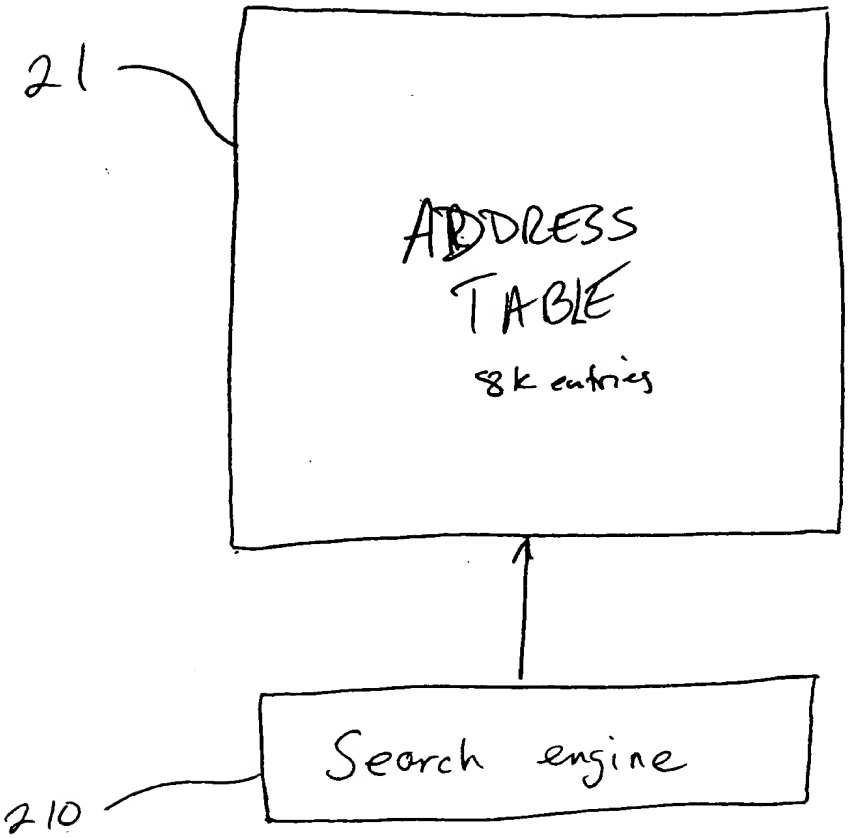


Fig 38

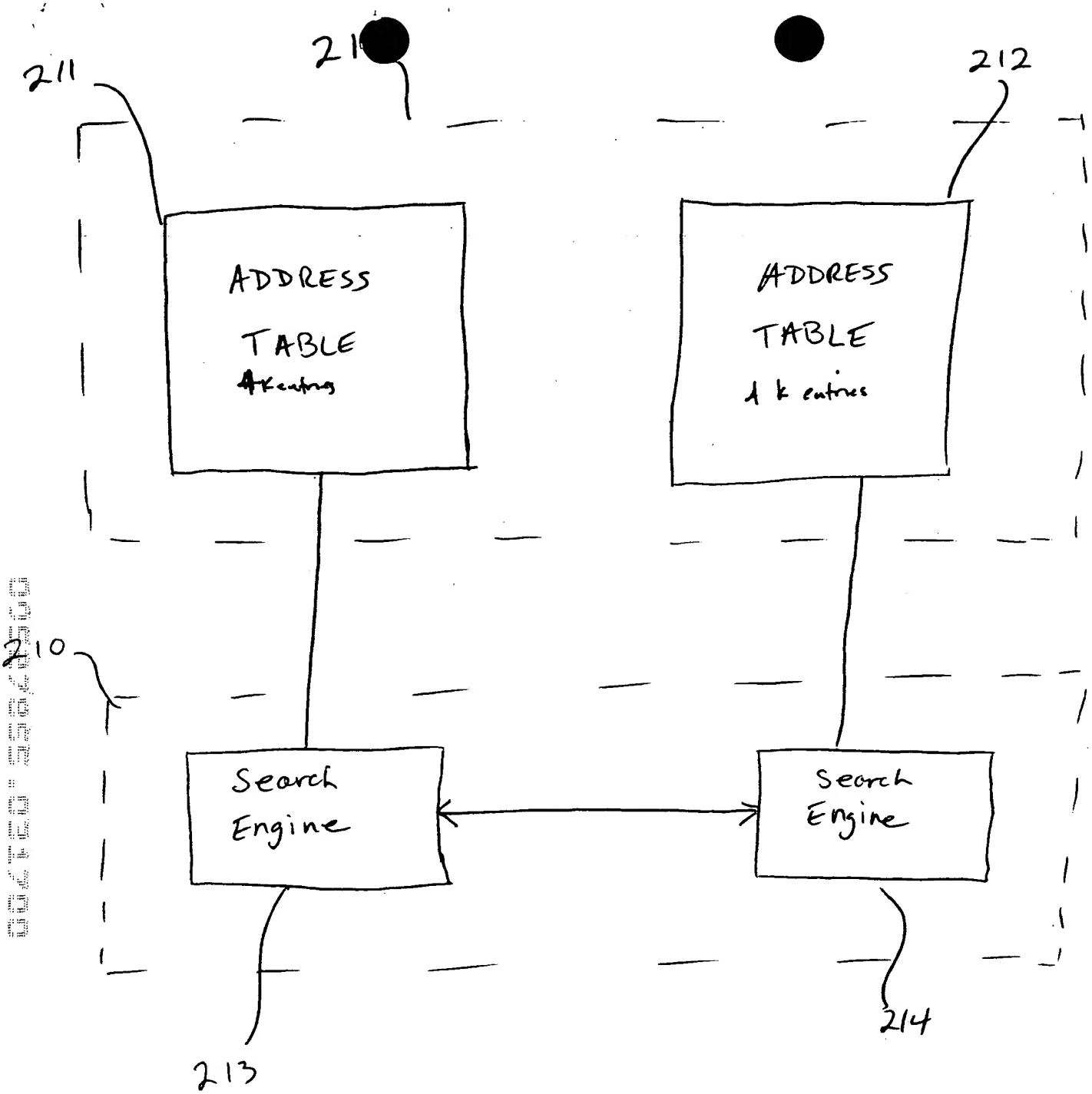

211
212
ADDRESS TABLE
ADDRESS TABLE
1 k entries
210
Search Engine
Search Engine
213
214

Fig. 39

Figure 40a

21

address	entry
31	AF
30	AE
29	AD
28	AC
27	AB
26	AA
25	Z
24	Y
23	X
22	W
21	V
20	U
19	T
18	S
17	R
16	Q
15	P
14	O
13	N
12	M
11	L
10	K
9	J
8	I
7	H
6	G
5	F
4	E
3	D
2	C
1	B
0	A

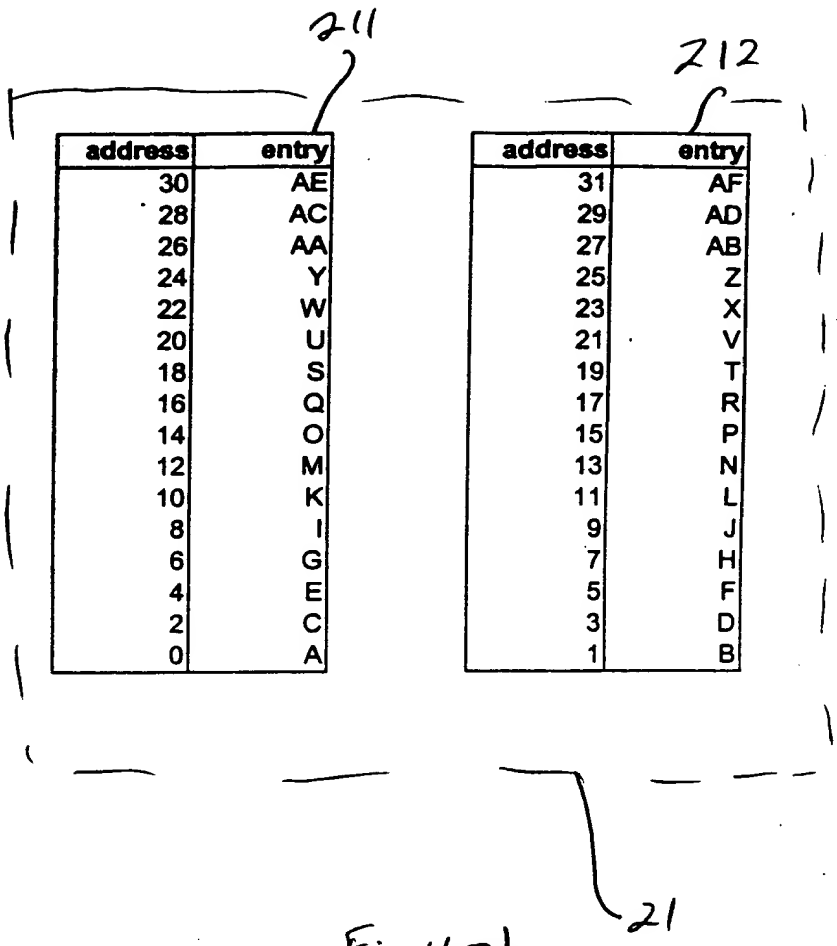


Fig 40b

Figure 4a

21

address	entry
31	NN
30	MM
29	LL
28	KK
27	JJ
26	GH
25	CF
24	CC
23	BE
22	BD
21	BC
20	BA
19	AC
18	AB
17	AA
16	Y
15	X
14	V
13	T
12	S
11	R
10	Q
9	N
8	M
7	L
6	K
5	J
4	G
3	E
2	D
1	C
0	B

211

address	entry
30	MM
28	KK
26	GH
24	CC
22	BD
20	BA
18	AB
16	Y
14	V
12	S
10	Q
8	M
6	K
4	G
2	D
0	B

212

address	entry
31	NN
29	LL
27	JJ
25	CF
23	BE
21	BC
19	AC
17	AA
15	X
13	T
11	R
9	N
7	L
5	J
3	E
1	C

Fig 4A

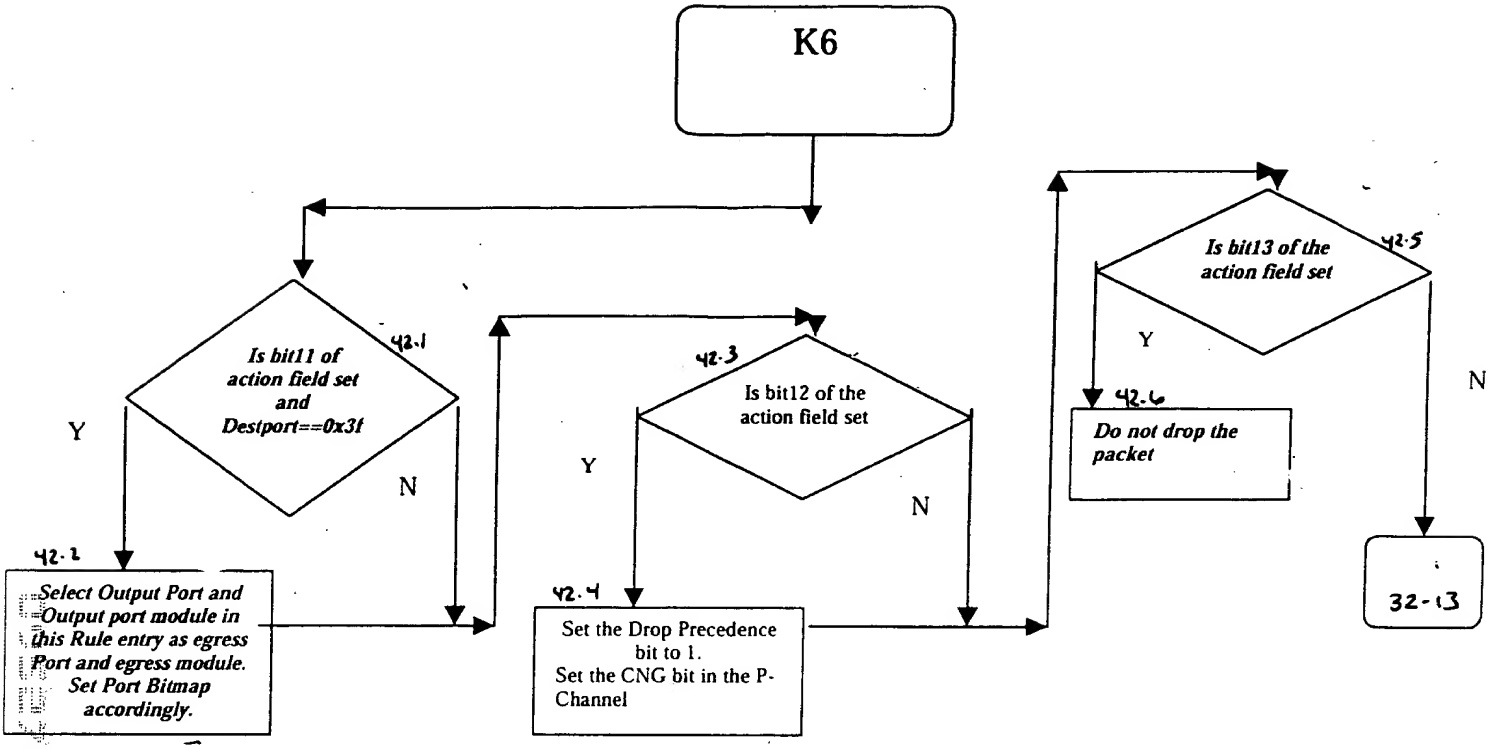

K6
Is bit11 of action field set and Destport==0x3f
42.1
Y
N
42.2
Select Output Port and Output port module in this Rule entry as egress Port and egress module. Set Port Bitmap accordingly.
Is bit12 of the action field set
42.3
Y
N
42.4
Set the Drop Precedence bit to 1.
Set the CNG bit in the P-Channel
Is bit13 of the action field set
42.5
Y
N
42.6
Do not drop the packet
32-13

Figure 42

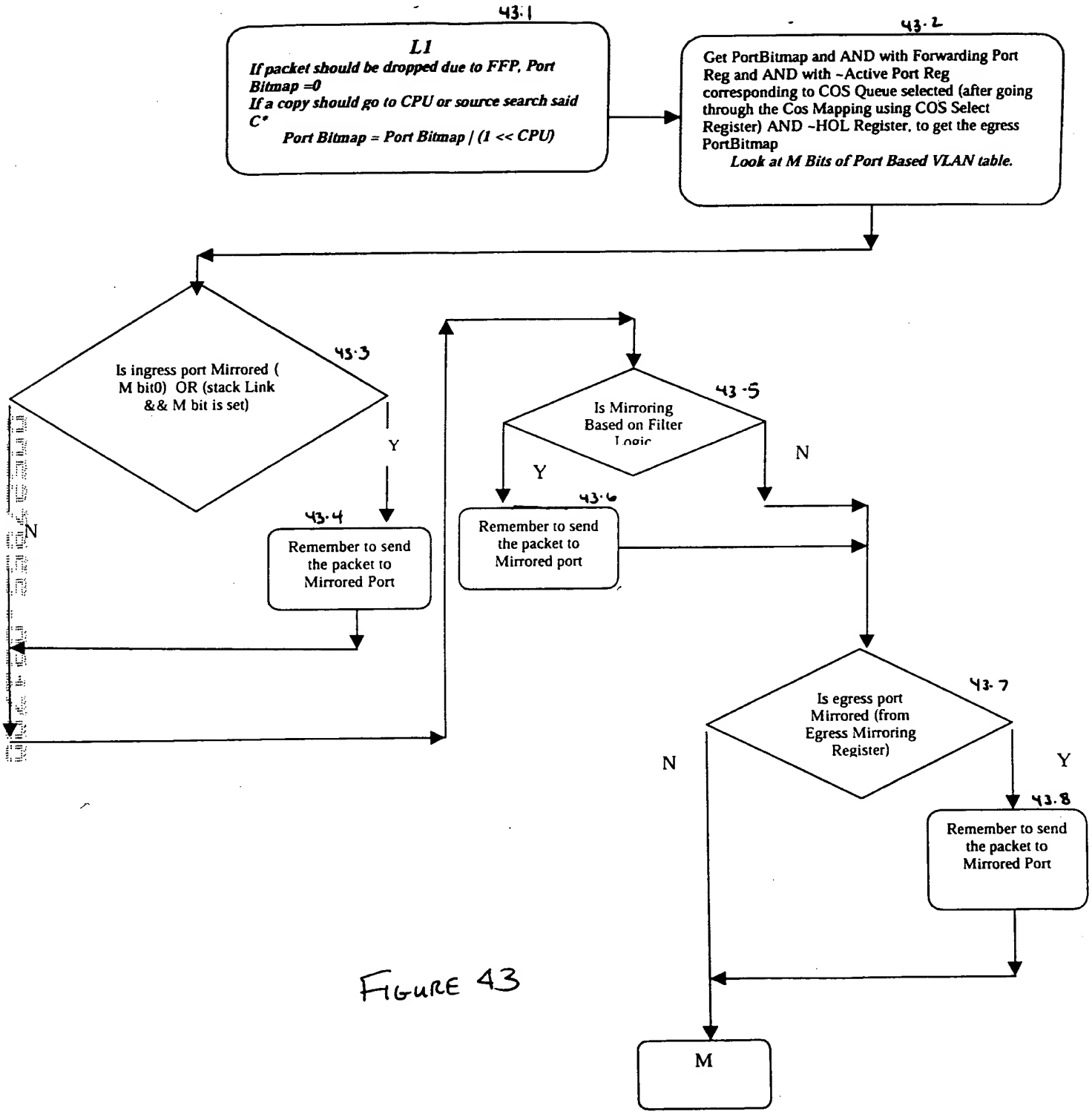

43.1
L1
If packet should be dropped due to FFP, Port Bitmap =0
If a copy should go to CPU or source search said C*
Port Bitmap = Port Bitmap | (1 << CPU)
43.2
Get PortBitmap and AND with Forwarding Port Reg and AND with ~Active Port Reg corresponding to COS Queue selected (after going through the Cos Mapping using COS Select Register) AND ~HOL Register, to get the egress PortBitmap
Look at M Bits of Port Based VLAN table.
43.3
Is ingress port Mirrored (M bit0) OR (stack Link && M bit is set)
Y
N
43.4
Remember to send the packet to Mirrored Port
43.5
Is Mirroring Based on Filter Logic
Y
N
43.6
Remember to send the packet to Mirrored port
43.7
Is egress port Mirrored (from Egress Mirroring Register)
N
Y
43.8
Remember to send the packet to Mirrored Port
M

FIGURE 43

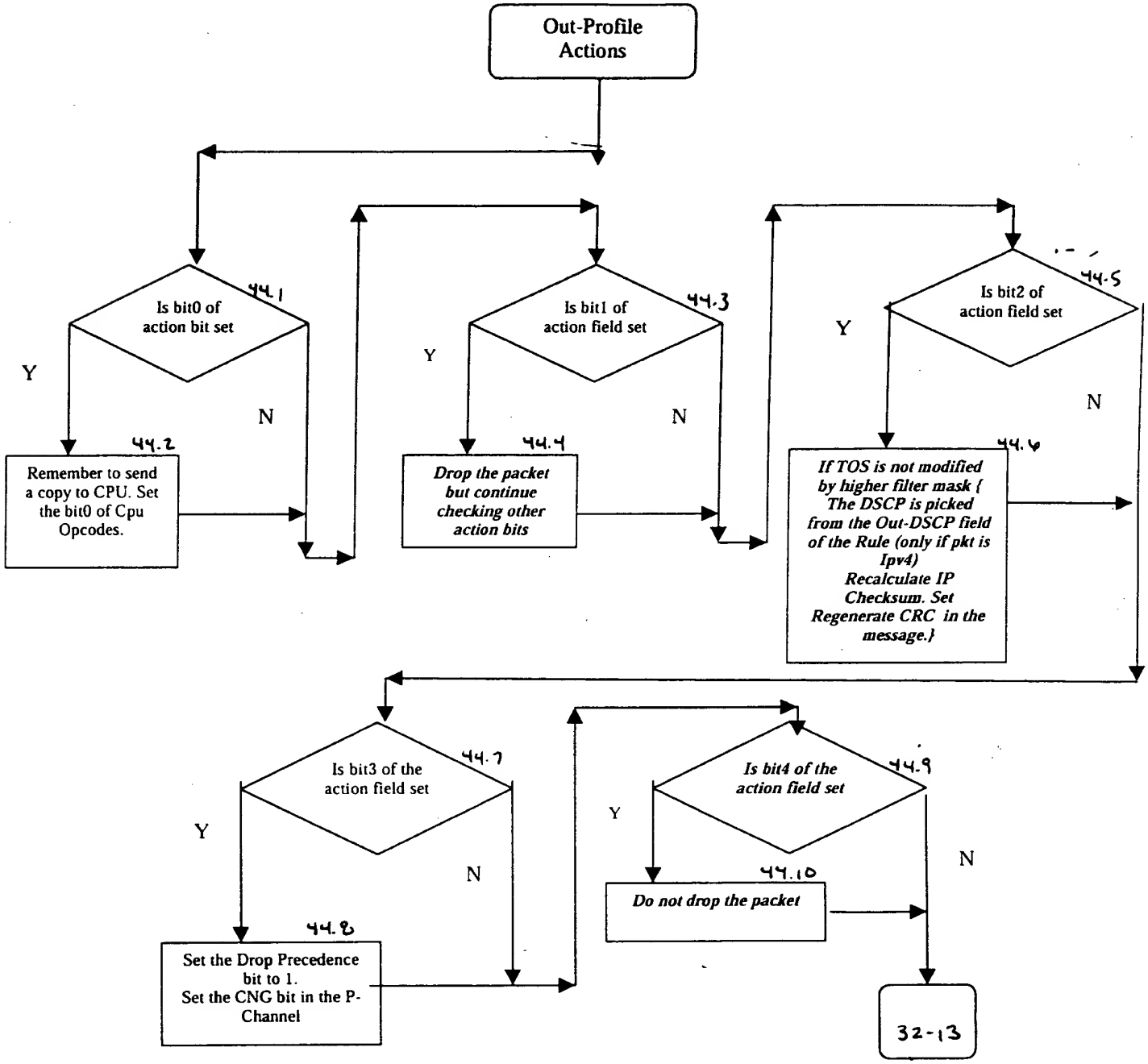

Out-Profile Actions
Is bit0 of action bit set
44.1
Y
N
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
44.2
Is bit1 of action field set
44.3
Y
N
Drop the packet but continue checking other action bits
44.4
Is bit2 of action field set
44.5
Y
N
If TOS is not modified by higher filter mask { The DSCP is picked from the Out-DSCP field of the Rule (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.}
44.6
Is bit3 of the action field set
44.7
Y
N
Set the Drop Precedence bit to 1. Set the CNG bit in the P-Channel
44.8
Is bit4 of the action field set
44.9
Y
N
Do not drop the packet
44.10
32-13

FIGURE 44

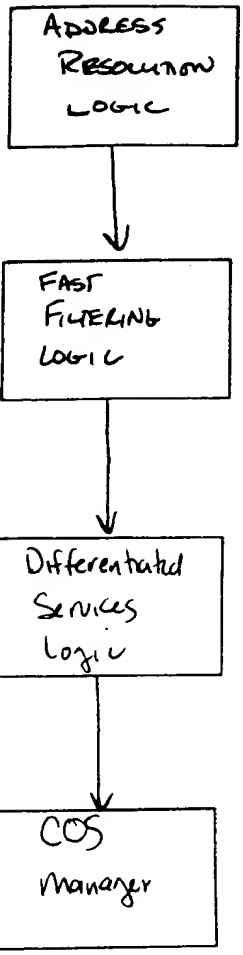

ADDRESS RESOLUTION LOGIC
FAST FILTERING LOGIC
Differentated Services Logic
COS Manager

FIGURE 45

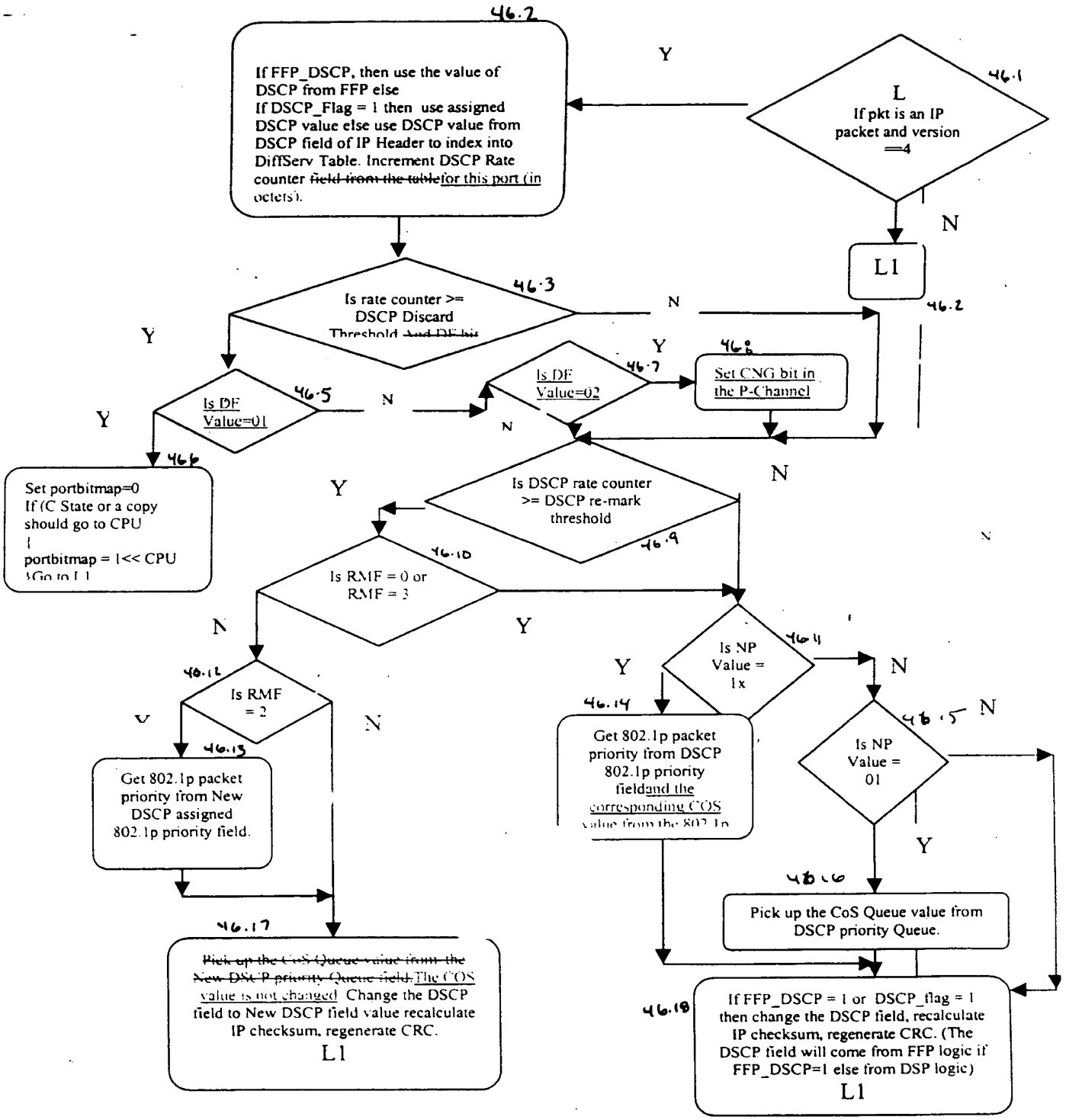

46.1
L
If pkt is an IP packet and version ==4
Y
N
L1
46.2
If FFP_DSCP, then use the value of DSCP from FFP else
If DSCP_Flag = 1 then use assigned DSCP value else use DSCP value from DSCP field of IP Header to index into DiffServ Table. Increment DSCP Rate counter ~~field from the table~~for this port (in octets).
46.3
Is rate counter >= DSCP Discard Threshold ~~And DE bit~~
Y
N
46.5
Is DE Value=01
Y
N
46.7
Is DE Value=02
Y
N
46.8
Set CNG bit in the P-Channel
46.6
Set portbitmap=0
If (C State or a copy should go to CPU
{
portbitmap = 1<< CPU
}Go to L1
Y
N
Is DSCP rate counter >= DSCP re-mark threshold
46.9
46.10
Is RMF = 0 or RMF = 3
N
Y
46.12
Is RMF = 2
Y
N
46.13
Get 802.1p packet priority from New DSCP assigned 802.1p priority field.
46.14
Get 802.1p packet priority from DSCP 802.1p priority fieldand the corresponding COS value from the 802.1p
46.4
Is NP Value = 1x
Y
N
46.15
Is NP Value = 01
N
Y
46.16
Pick up the CoS Queue value from DSCP priority Queue.
46.17
~~Pick up the CoS Queue value from the New DSCP priority Queue field.~~The COS value is not changed Change the DSCP field to New DSCP field value recalculate IP checksum, regenerate CRC.
L1
46.18
If FFP_DSCP = 1 or DSCP_flag = 1 then change the DSCP field, recalculate IP checksum, regenerate CRC. (The DSCP field will come from FFP logic if FFP_DSCP=1 else from DSP logic)
L1

FIGURE 46

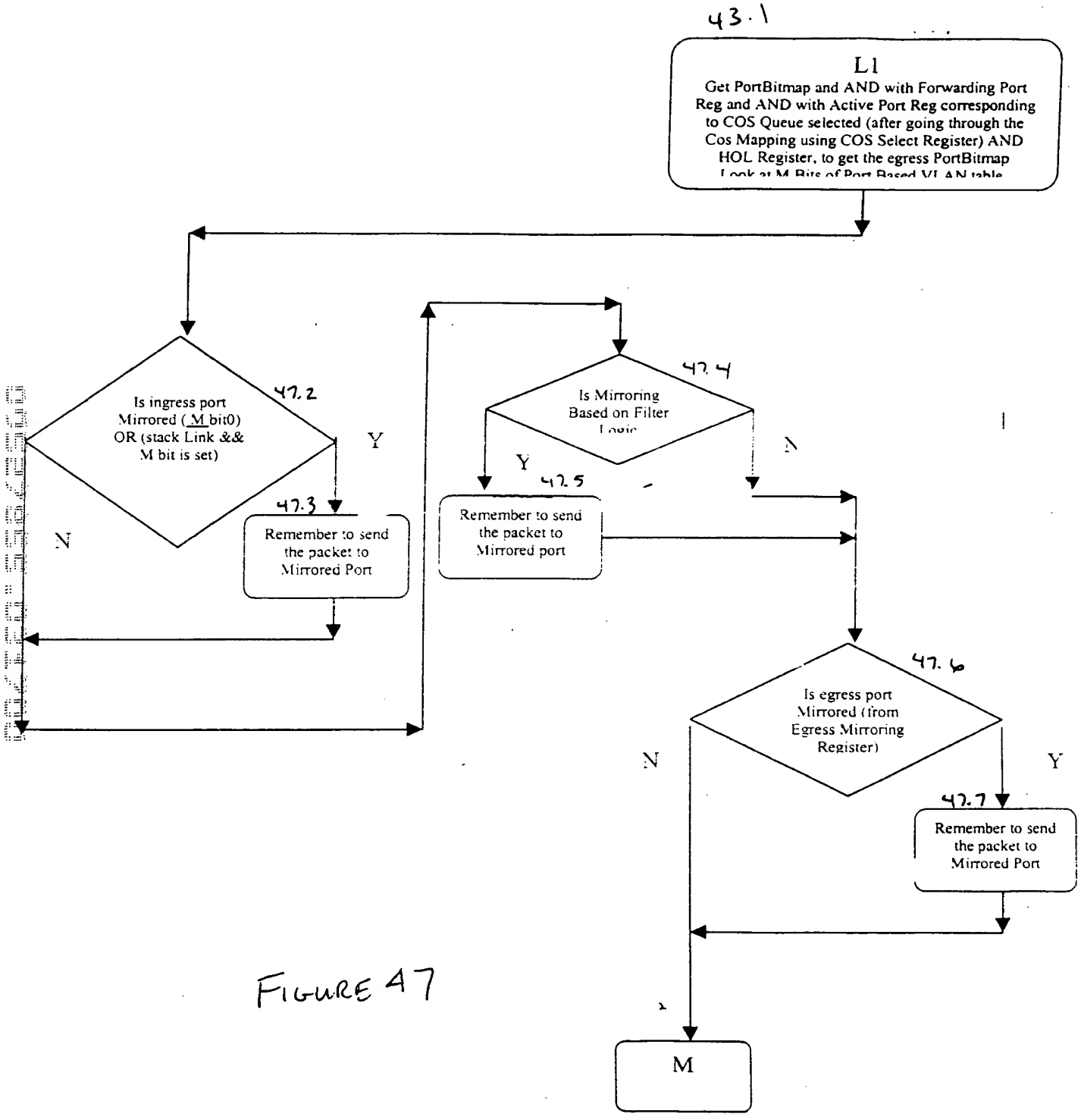
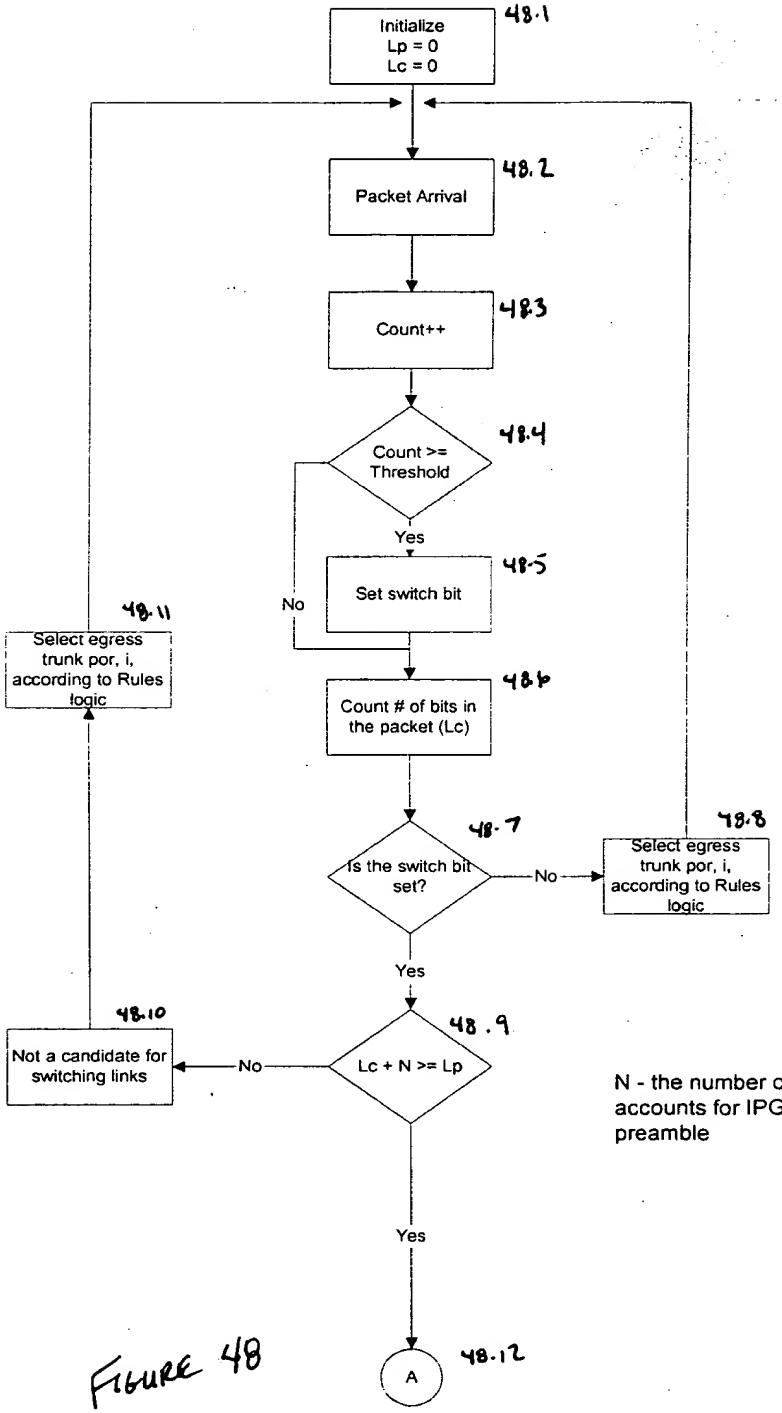

43.1
L1
Get PortBitmap and AND with Forwarding Port Reg and AND with Active Port Reg corresponding to COS Queue selected (after going through the Cos Mapping using COS Select Register) AND HOL Register, to get the egress PortBitmap
Look at M Bits of Port Based VLAN table
47.2
Is ingress port Mirrored (M bit0) OR (stack Link && M bit is set)
Y
N
47.3
Remember to send the packet to Mirrored Port
47.4
Is Mirroring Based on Filter Logic
Y
N
47.5
Remember to send the packet to Mirrored port
47.6
Is egress port Mirrored (from Egress Mirroring Register)
N
Y
47.7
Remember to send the packet to Mirrored Port
M

FIGURE 47



N - the number of bits accounts for IPG and preamble

FIGURE 48

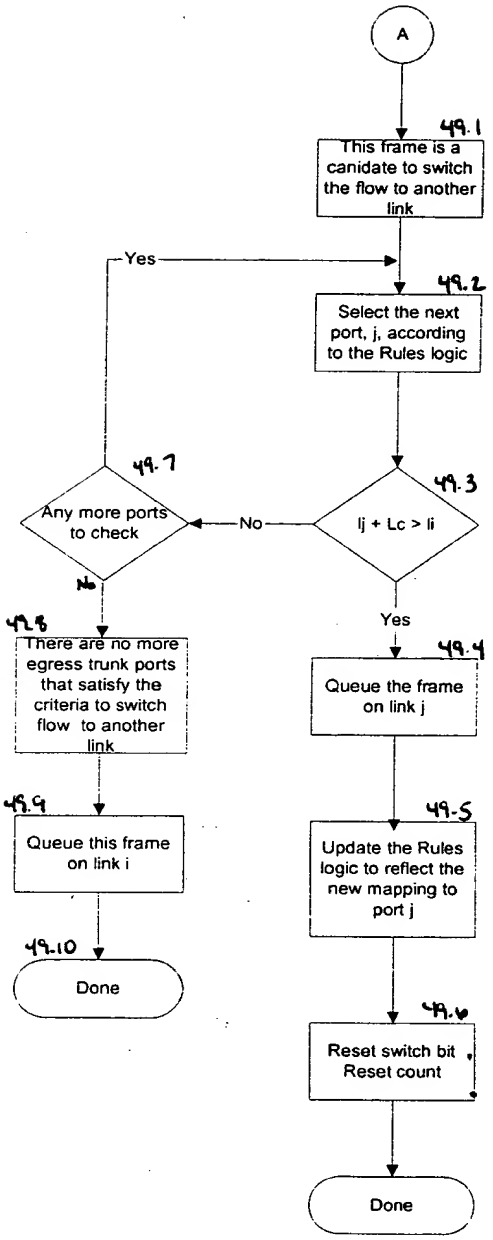

A
49.1
This frame is a canidate to switch the flow to another link
Yes
49.2
Select the next port, j, according to the Rules logic
49.7
Any more ports to check
No
49.3
Ij + Lc > Ii
No
Yes
49.8
There are no more egress trunk ports that satisfy the criteria to switch flow to another link
49.4
Queue the frame on link j
49.9
Queue this frame on link i
49.5
Update the Rules logic to reflect the new mapping to port j
49.10
Done
49.6
Reset switch bit
Reset count
Done

FIGURE 49

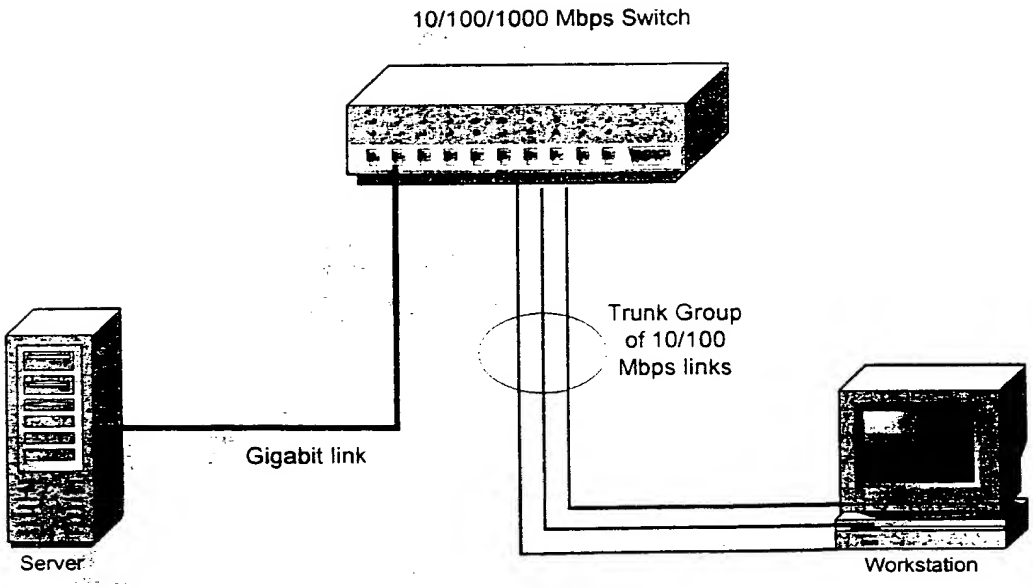

10/100/1000 Mbps Switch
Trunk Group
of 10/100
Mbps links
Gigabit link
Server
Workstation

FIGURE 50

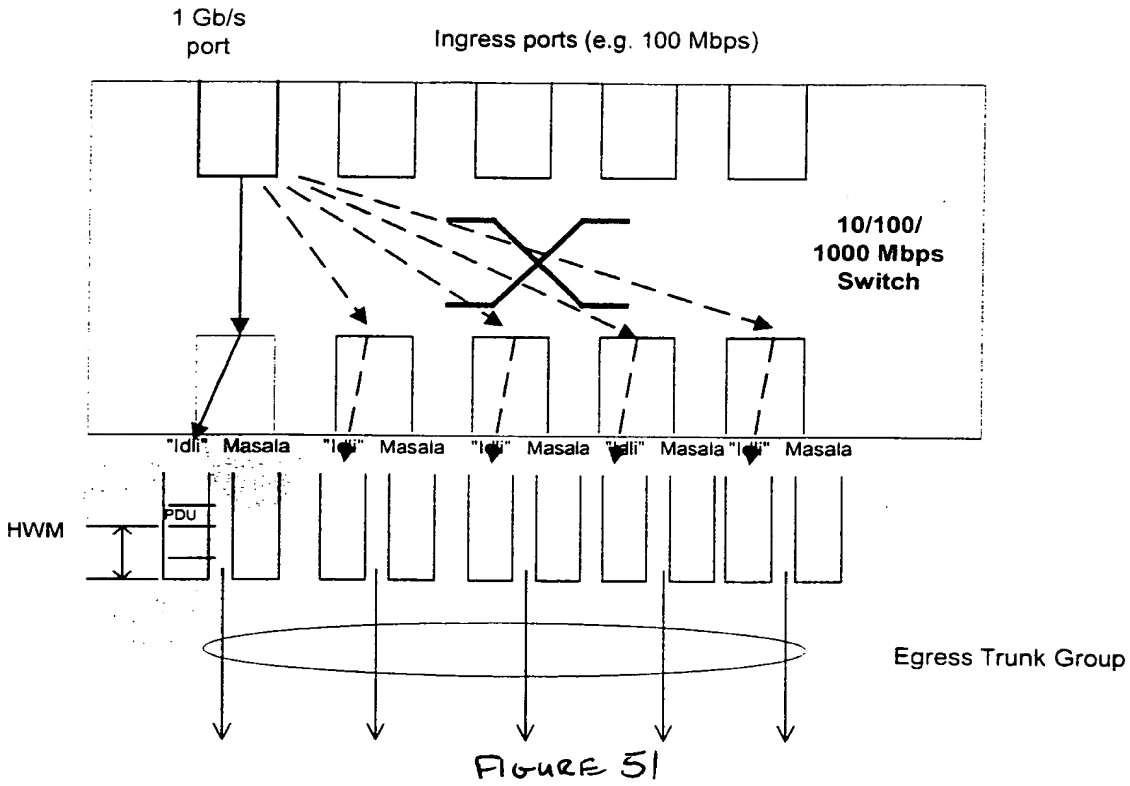

1 Gb/s
port
Ingress ports (e.g. 100 Mbps)
10/100/
1000 Mbps
Switch
"Idli" Masala "Idli" Masala "Idli" Masala "Idli" Masala "Idli" Masala
HWM
PDU
Egress Trunk Group

FIGURE 51

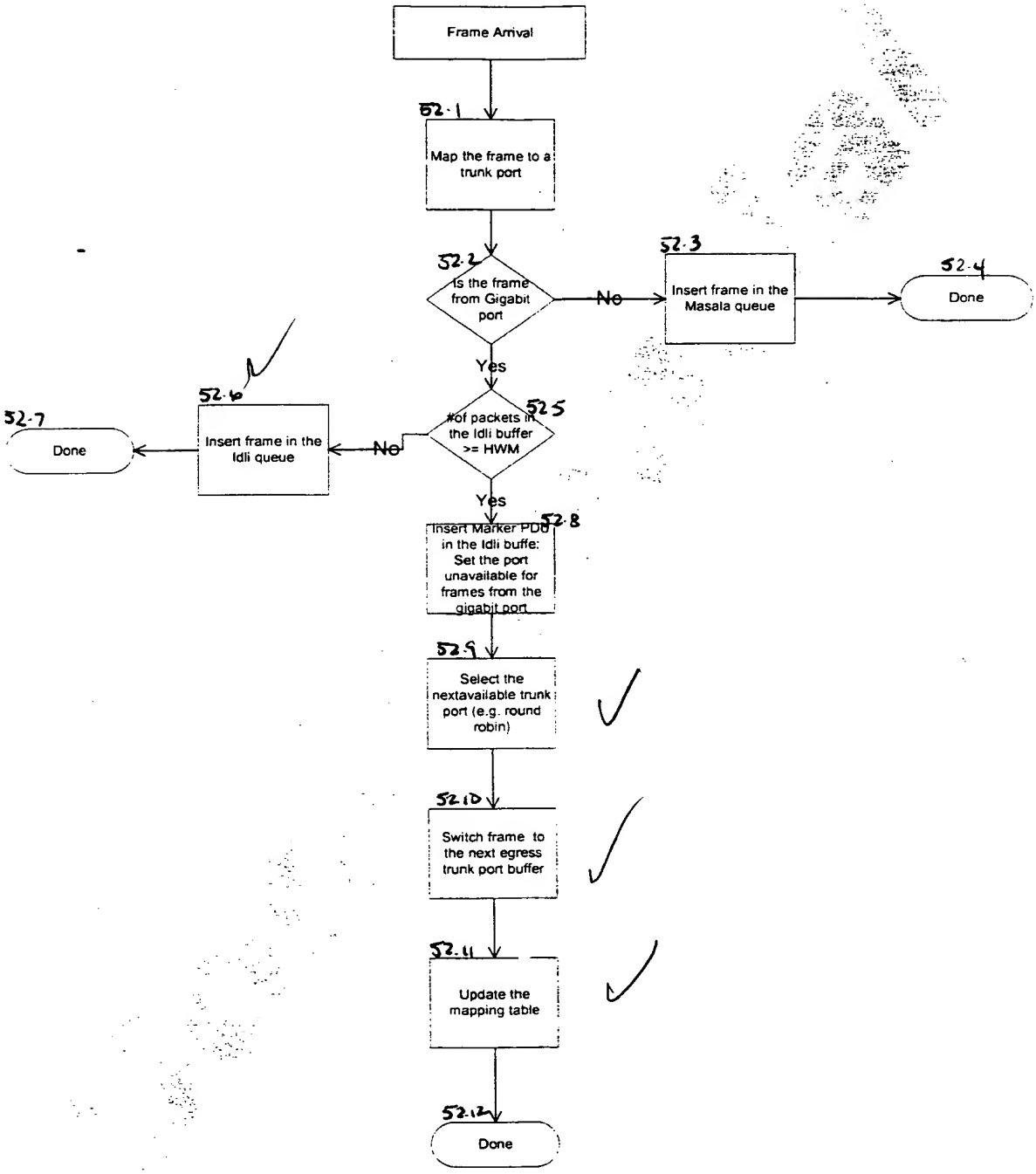

Frame Arrival
52.1
Map the frame to a trunk port
52.2
Is the frame from Gigabit port
No
52.3
Insert frame in the Masala queue
52.4
Done
Yes
52.5
#of packets in the Idli buffer >= HWM
No
52.6
Insert frame in the Idli queue
52.7
Done
Yes
52.8
Insert Marker PDU in the Idli buffe: Set the port unavailable for frames from the gigabit port
52.9
Select the nextavailable trunk port (e.g. round robin)
52.10
Switch frame to the next egress trunk port buffer
52.11
Update the mapping table
52.12
Done

FIGURE 52

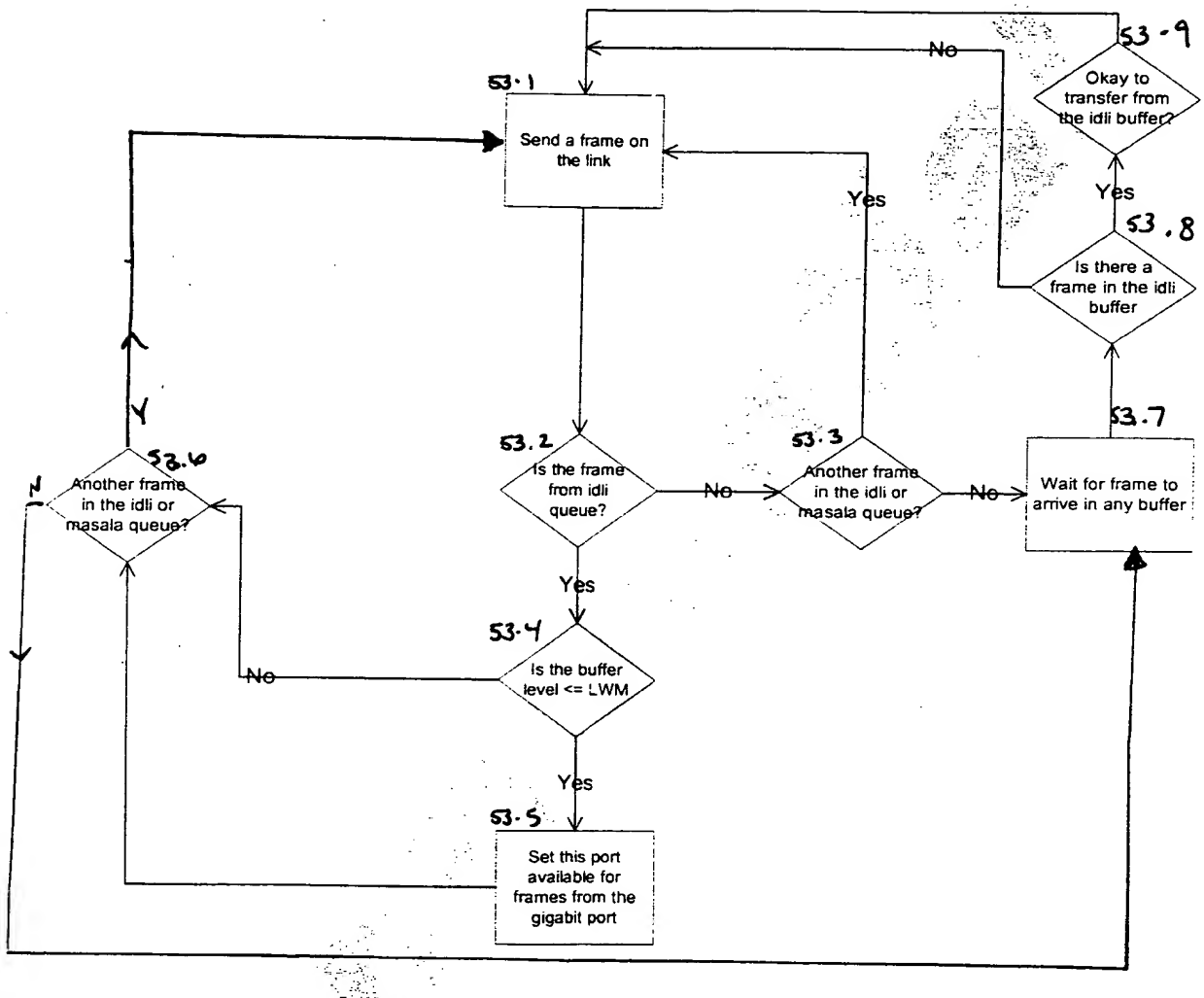

53.1
Send a frame on the link
53.2
Is the frame from idli queue?
53.3
Another frame in the idli or masala queue?
53.4
Is the buffer level <= LWM
53.5
Set this port available for frames from the gigabit port
53.6
Another frame in the idli or masala queue?
53.7
Wait for frame to arrive in any buffer
53.8
Is there a frame in the idli buffer
53.9
Okay to transfer from the idli buffer?
Yes
No
Y
N

FIGURE 53

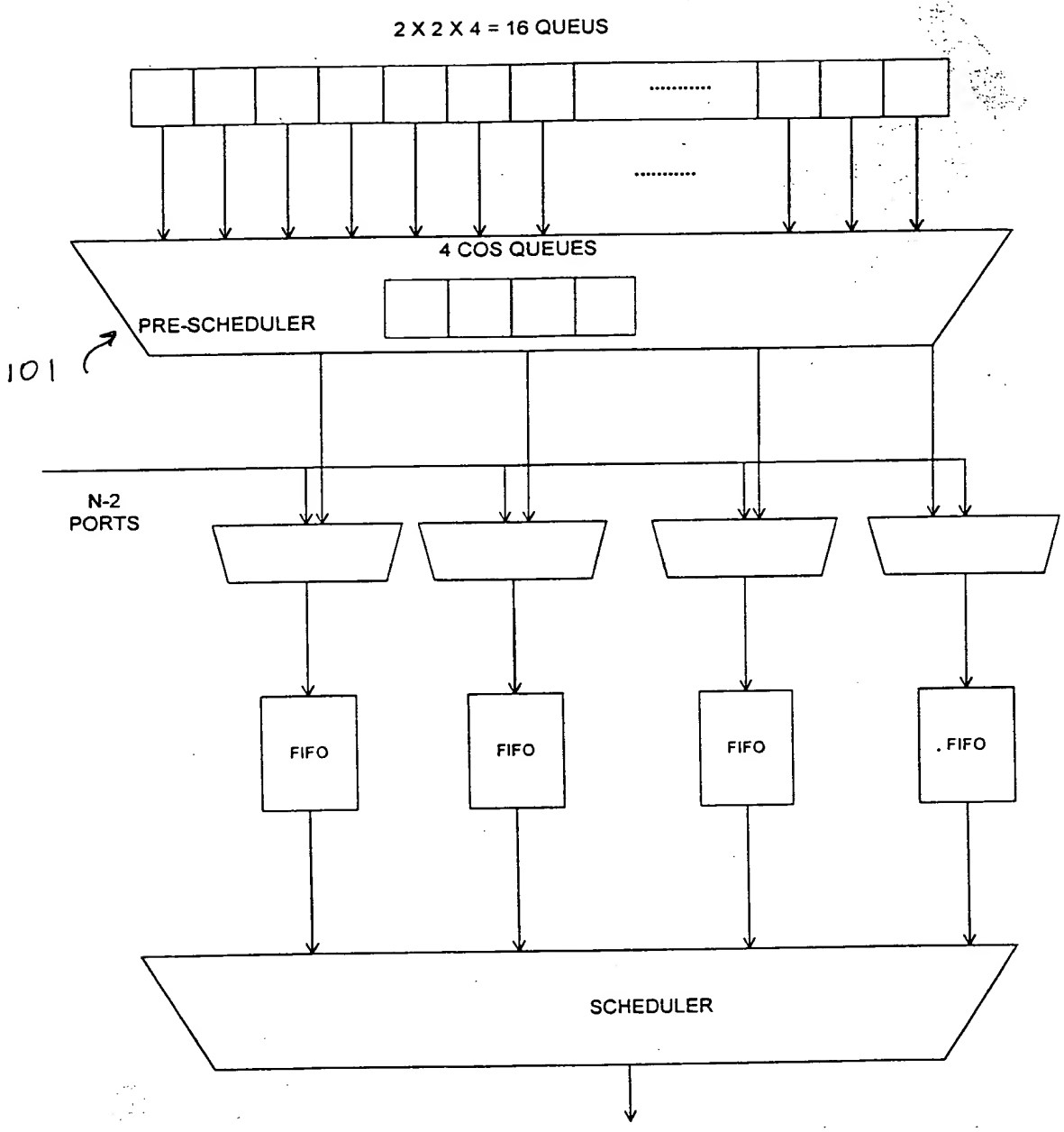

2 X 2 X 4 = 16 QUEUS
4 COS QUEUES
PRE-SCHEDULER
101
N-2
PORTS
FIFO
FIFO
FIFO
. FIFO
SCHEDULER

FIGURE 54

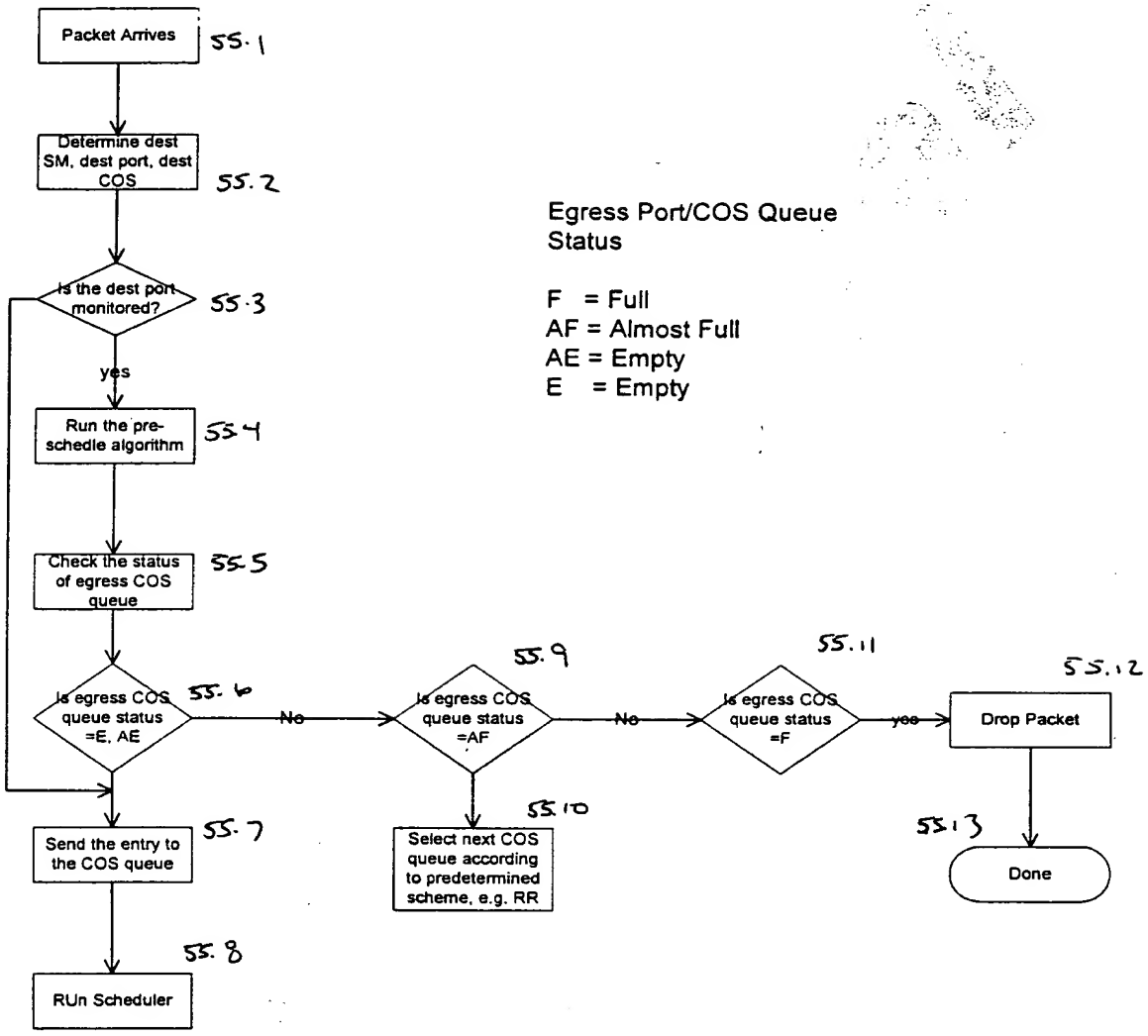
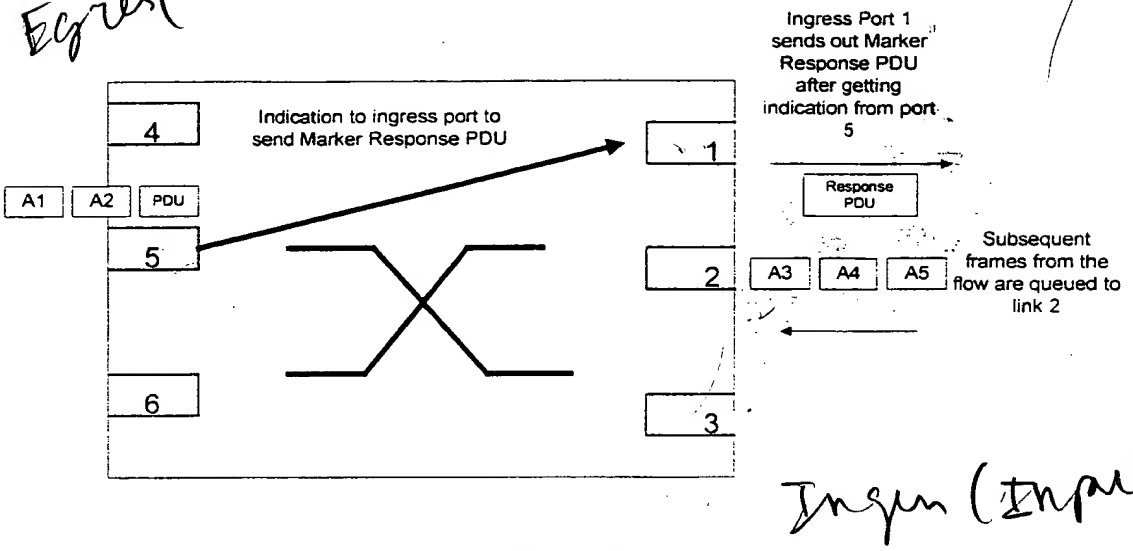

Packet Arrives
55.1
Determine dest SM, dest port, dest COS
55.2
Is the dest port monitored?
55.3
yes
Run the pre-schedle algorithm
55.4
Check the status of egress COS queue
55.5
Is egress COS queue status =E, AE
55.6
No
Is egress COS queue status =AF
55.9
No
Is egress COS queue status =F
55.11
yes
Drop Packet
55.12
Send the entry to the COS queue
55.7
Select next COS queue according to predetermined scheme, e.g. RR
55.10
RUn Scheduler
55.8
Done
55.13
Egress Port/COS Queue Status
F = Full
AF = Almost Full
AE = Empty
E = Empty

FIGURE 55

Egres (output)



Ingress (Input)

FIGURE 5b